Relationship between: ("'Data Structure & Algorithm'", "'Introduction to Computing'")

['Good marks in Introduction to Computing']=>['Good marks in Data Structure & Algorithm']: 85.29411764705883%

['Average marks in Introduction to Computing']=>['Average marks in Data Structure & Algorithm']: 90.0%

Relationship between: ("'Introduction to Computing'", "'Numerical Methods'")

['Good marks in Introduction to Computing']=>['Good marks in Numerical Methods']: 85.29411764705883%

['Average marks in Introduction to Computing']=>['Average marks in Numerical Methods']: 92.5%

['Poor marks in Introduction to Computing']=>['Poor marks in Numerical Methods']: 89.47368421052632%

Relationship between: ("'Design & Analysis of Algorithm'", "'Introduction to Computing'")

['Good marks in Introduction to Computing']=>['Good marks in Design & Analysis of Algorithm']: 85.29411764705883%

['Average marks in Introduction to Computing']=>['Average marks in Design & Analysis of Algorithm']: 95.0%

Relationship between: ("'Introduction to Computing'", "'Object Oriented Programming'")

['Good marks in Introduction to Computing']=>['Good marks in Object Oriented Programming']: 85.29411764705883%

['Average marks in Introduction to Computing']=>['Average marks in Object Oriented Programming']: 95.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Organisation'")

['Good marks in Analog & Digital Electronics']=>['Good marks in Computer Organisation']: 96.42857142857143%

['Average marks in Analog & Digital Electronics']=>['Average marks in Computer Organisation']: 100.0%

['Poor marks in Analog & Digital Electronics']=>['Poor marks in Computer Organisation']: 96.42857142857143%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'")

['Good marks in Analog & Digital Electronics']=>['Good marks in Computer Architecture']: 96.42857142857143%

['Average marks in Analog & Digital Electronics']=>['Average marks in Computer Architecture']: 100.0%

['Poor marks in Analog & Digital Electronics']=>['Poor marks in Computer Architecture']: 92.85714285714286%

Relationship between: ("'Analog & Digital Electronics'", "'Microprocessors & Microcontrollers'")

['Good marks in Analog & Digital Electronics']=>['Good marks in Microprocessors & Microcontrollers']: 96.42857142857143%

['Average marks in Analog & Digital Electronics']=>['Average marks in Microprocessors & Microcontrollers']: 96.42857142857143%

['Poor marks in Analog & Digital Electronics']=>['Poor marks in Microprocessors & Microcontrollers']: 92.85714285714286%

Relationship between: ("'Data Structure & Algorithm'", "'Numerical Methods'")

['Good marks in Data Structure & Algorithm']=>['Good marks in Numerical Methods']: 80.0%

['Poor marks in Data Structure & Algorithm']=>['Poor marks in Numerical Methods']: 88.23529411764706%

Relationship between: ("'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'")

['Good marks in Data Structure & Algorithm']=>['Good marks in Design & Analysis of Algorithm']: 88.57142857142857%

['Average marks in Data Structure & Algorithm']=>['Average marks in Design & Analysis of Algorithm']: 98.33333333333333%

['Poor marks in Data Structure & Algorithm']=>['Poor marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Data Structure & Algorithm'", "'Object Oriented Programming'")

['Good marks in Data Structure & Algorithm']=>['Good marks in Object Oriented Programming']: 88.57142857142857%

['Average marks in Data Structure & Algorithm']=>['Average marks in Object Oriented Programming']: 98.33333333333333%

['Poor marks in Data Structure & Algorithm']=>['Poor marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'")

['Good marks in Computer Organisation']=>['Good marks in Computer Architecture']: 94.54545454545455%

['Average marks in Computer Organisation']=>['Average marks in Computer Architecture']: 96.55172413793103%

['Poor marks in Computer Organisation']=>['Poor marks in Computer Architecture']: 89.28571428571429%

Relationship between: ("'Computer Organisation'", "'Microprocessors & Microcontrollers'")

['Good marks in Computer Organisation']=>['Good marks in Microprocessors & Microcontrollers']: 96.36363636363636%

['Average marks in Computer Organisation']=>['Average marks in Microprocessors & Microcontrollers']: 93.10344827586206%

['Poor marks in Computer Organisation']=>['Poor marks in Microprocessors & Microcontrollers']: 89.28571428571429%

Relationship between: ("'Design & Analysis of Algorithm'", "'Numerical Methods'")

['Good marks in Numerical Methods']=>['Good marks in Design & Analysis of Algorithm']: 85.29411764705883%

['Average marks in Numerical Methods']=>['Average marks in Design & Analysis of Algorithm']: 97.43589743589743%

Relationship between: ("'Numerical Methods'", "'Object Oriented Programming'")

['Good marks in Numerical Methods']=>['Good marks in Object Oriented Programming']: 85.29411764705883%

['Average marks in Numerical Methods']=>['Average marks in Object Oriented Programming']: 97.43589743589743%

Relationship between: ("'Compiler Design'", "'Formal Language & Automata Theory'")

['Good marks in Formal Language & Automata Theory']=>['Good marks in Compiler Design']: 93.87755102040816%

['Average marks in Formal Language & Automata Theory']=>['Average marks in Compiler Design']: 95.1219512195122%

['Poor marks in Formal Language & Automata Theory']=>['Poor marks in Compiler Design']: 95.45454545454545%

Relationship between: ("'Computer Architecture'", "'Microprocessors & Microcontrollers'")

['Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 92.85714285714286%

['Average marks in Computer Architecture']=>['Average marks in Microprocessors & Microcontrollers']: 96.42857142857143%

['Poor marks in Computer Architecture']=>['Poor marks in Microprocessors & Microcontrollers']: 85.71428571428571%

Relationship between: ("'Design & Analysis of Algorithm'", "'Object Oriented Programming'")

['Good marks in Design & Analysis of Algorithm']=>['Good marks in Object Oriented Programming']: 100.0%

['Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

['Poor marks in Design & Analysis of Algorithm']=>['Poor marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Data Structure & Algorithm'", "'Introduction to Computing'")

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Data Structure & Algorithm']: 83.33333333333334%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Data Structure & Algorithm']: 90.47619047619048%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Organisation'", "'Introduction to Computing'")

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Organisation']: 94.44444444444444%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Organisation']: 95.23809523809523%

['Poor marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Organisation']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Introduction to Computing'", "'Numerical Methods'")

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Numerical Methods']: 83.33333333333334%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Numerical Methods']: 90.47619047619048%

['Poor marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Poor marks in Numerical Methods']: 94.11764705882352%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Introduction to Computing'")

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Architecture']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Architecture']: 100.0%

['Poor marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Architecture']: 88.23529411764706%

Relationship between: ("'Analog & Digital Electronics'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'")

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Design & Analysis of Algorithm']: 83.33333333333334%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'")

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Microprocessors & Microcontrollers']: 94.44444444444444%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Microprocessors & Microcontrollers']: 95.23809523809523%

['Poor marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Introduction to Computing'", "'Object Oriented Programming'")

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Object Oriented Programming']: 83.33333333333334%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Data Structure & Algorithm'", "'Introduction to Computing'", "'Numerical Methods'")

['Good marks in Introduction to Computing']=>['Good marks in Data Structure & Algorithm', 'Good marks in Numerical Methods']: 76.47058823529412%

['Good marks in Introduction to Computing', 'Good marks in Data Structure & Algorithm']=>['Good marks in Numerical Methods']: 89.65517241379311%

['Average marks in Introduction to Computing']=>['Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods']: 85.0%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm']=>['Average marks in Numerical Methods']: 94.44444444444444%

['Poor marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm']=>['Poor marks in Numerical Methods']: 94.73684210526315%

['Poor marks in Introduction to Computing', 'Poor marks in Data Structure & Algorithm']=>['Poor marks in Numerical Methods']: 88.23529411764706%

Relationship between: ("'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'")

['Good marks in Introduction to Computing']=>['Good marks in Data Structure & Algorithm', 'Good marks in Design & Analysis of Algorithm']: 82.35294117647058%

['Good marks in Introduction to Computing', 'Good marks in Data Structure & Algorithm']=>['Good marks in Design & Analysis of Algorithm']: 96.55172413793103%

['Average marks in Introduction to Computing']=>['Average marks in Data Structure & Algorithm', 'Average marks in Design & Analysis of Algorithm']: 90.0%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

['Poor marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

['Poor marks in Introduction to Computing', 'Poor marks in Data Structure & Algorithm']=>['Poor marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Data Structure & Algorithm'", "'Introduction to Computing'", "'Object Oriented Programming'")

['Good marks in Introduction to Computing']=>['Good marks in Data Structure & Algorithm', 'Good marks in Object Oriented Programming']: 82.35294117647058%

['Good marks in Introduction to Computing', 'Good marks in Data Structure & Algorithm']=>['Good marks in Object Oriented Programming']: 96.55172413793103%

['Average marks in Introduction to Computing']=>['Average marks in Data Structure & Algorithm', 'Average marks in Object Oriented Programming']: 90.0%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

['Poor marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

['Poor marks in Introduction to Computing', 'Poor marks in Data Structure & Algorithm']=>['Poor marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Introduction to Computing'")

['Good marks in Introduction to Computing', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture']: 100.0%

['Poor marks in Introduction to Computing', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture']: 83.33333333333334%

Relationship between: ("'Computer Organisation'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'")

['Good marks in Introduction to Computing', 'Good marks in Computer Organisation']=>['Good marks in Microprocessors & Microcontrollers']: 94.11764705882352%

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

['Poor marks in Introduction to Computing', 'Good marks in Computer Organisation']=>['Good marks in Microprocessors & Microcontrollers']: 94.44444444444444%

Relationship between: ("'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Numerical Methods'")

['Good marks in Introduction to Computing']=>['Good marks in Numerical Methods', 'Good marks in Design & Analysis of Algorithm']: 79.41176470588235%

['Good marks in Introduction to Computing', 'Good marks in Numerical Methods']=>['Good marks in Design & Analysis of Algorithm']: 93.10344827586206%

['Average marks in Introduction to Computing']=>['Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']: 90.0%

['Average marks in Introduction to Computing', 'Average marks in Numerical Methods']=>['Average marks in Design & Analysis of Algorithm']: 97.2972972972973%

Relationship between: ("'Introduction to Computing'", "'Numerical Methods'", "'Object Oriented Programming'")

['Good marks in Introduction to Computing']=>['Good marks in Numerical Methods', 'Good marks in Object Oriented Programming']: 79.41176470588235%

['Good marks in Introduction to Computing', 'Good marks in Numerical Methods']=>['Good marks in Object Oriented Programming']: 93.10344827586206%

['Average marks in Introduction to Computing']=>['Average marks in Numerical Methods', 'Average marks in Object Oriented Programming']: 90.0%

['Average marks in Introduction to Computing', 'Average marks in Numerical Methods']=>['Average marks in Object Oriented Programming']: 97.2972972972973%

Relationship between: ("'Computer Architecture'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'")

['Good marks in Introduction to Computing', 'Good marks in Computer Architecture']=>['Good marks in Design & Analysis of Algorithm']: 84.21052631578947%

['Average marks in Introduction to Computing', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Computer Architecture'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'")

['Good marks in Introduction to Computing', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 89.47368421052632%

['Average marks in Introduction to Computing', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 90.9090909090909%

['Poor marks in Introduction to Computing', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Computer Architecture'", "'Introduction to Computing'", "'Object Oriented Programming'")

['Good marks in Introduction to Computing', 'Good marks in Computer Architecture']=>['Good marks in Object Oriented Programming']: 84.21052631578947%

['Average marks in Introduction to Computing', 'Good marks in Computer Architecture']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Object Oriented Programming'")

['Good marks in Introduction to Computing']=>['Good marks in Design & Analysis of Algorithm', 'Good marks in Object Oriented Programming']: 85.29411764705883%

['Good marks in Introduction to Computing', 'Good marks in Design & Analysis of Algorithm']=>['Good marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 95.0%

['Average marks in Introduction to Computing', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

['Poor marks in Introduction to Computing', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

['Poor marks in Introduction to Computing', 'Poor marks in Design & Analysis of Algorithm']=>['Poor marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Introduction to Computing'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Good marks in Introduction to Computing', 'Good marks in Microprocessors & Microcontrollers']=>['Good marks in Object Oriented Programming']: 88.88888888888889%

['Average marks in Introduction to Computing', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Organisation'", "'Introduction to Computing'", "'Numerical Methods'")

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation']=>['Average marks in Numerical Methods']: 90.0%

['Poor marks in Introduction to Computing', 'Good marks in Computer Organisation']=>['Poor marks in Numerical Methods']: 94.44444444444444%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Organisation'", "'Data Structure & Algorithm'")

['Good marks in Analog & Digital Electronics', 'Good marks in Data Structure & Algorithm']=>['Good marks in Computer Organisation']: 94.44444444444444%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Good marks in Computer Organisation']: 96.875%

['Average marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Average marks in Computer Organisation']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Data Structure & Algorithm'")

['Good marks in Analog & Digital Electronics', 'Good marks in Data Structure & Algorithm']=>['Good marks in Computer Architecture']: 100.0%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Good marks in Computer Architecture']: 96.875%

['Average marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Average marks in Computer Architecture']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Data Structure & Algorithm'", "'Microprocessors & Microcontrollers'")

['Good marks in Analog & Digital Electronics', 'Good marks in Data Structure & Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 94.44444444444444%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 96.875%

Relationship between: ("'Analog & Digital Electronics'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'")

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Average marks in Design & Analysis of Algorithm']: 96.875%

['Average marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Data Structure & Algorithm'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Average marks in Object Oriented Programming']: 96.875%

['Average marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Computer Organisation'")

['Good marks in Analog & Digital Electronics']=>['Good marks in Computer Organisation', 'Good marks in Computer Architecture']: 92.85714285714286%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture']: 96.29629629629629%

['Average marks in Analog & Digital Electronics']=>['Average marks in Computer Organisation', 'Average marks in Computer Architecture']: 100.0%

['Average marks in Analog & Digital Electronics', 'Average marks in Computer Organisation']=>['Average marks in Computer Architecture']: 100.0%

['Poor marks in Analog & Digital Electronics']=>['Poor marks in Computer Organisation', 'Poor marks in Computer Architecture']: 89.28571428571429%

['Poor marks in Analog & Digital Electronics', 'Poor marks in Computer Organisation']=>['Poor marks in Computer Architecture']: 92.5925925925926%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Organisation'", "'Microprocessors & Microcontrollers'")

['Good marks in Analog & Digital Electronics']=>['Good marks in Computer Organisation', 'Good marks in Microprocessors & Microcontrollers']: 94.64285714285714%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation']=>['Good marks in Microprocessors & Microcontrollers']: 98.14814814814815%

['Average marks in Analog & Digital Electronics']=>['Average marks in Computer Organisation', 'Average marks in Microprocessors & Microcontrollers']: 96.42857142857143%

['Average marks in Analog & Digital Electronics', 'Average marks in Computer Organisation']=>['Average marks in Microprocessors & Microcontrollers']: 96.42857142857143%

['Poor marks in Analog & Digital Electronics']=>['Poor marks in Computer Organisation', 'Poor marks in Microprocessors & Microcontrollers']: 89.28571428571429%

['Poor marks in Analog & Digital Electronics', 'Poor marks in Computer Organisation']=>['Poor marks in Microprocessors & Microcontrollers']: 92.5925925925926%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Numerical Methods'")

['Good marks in Analog & Digital Electronics', 'Good marks in Numerical Methods']=>['Good marks in Computer Architecture']: 100.0%

['Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture']: 100.0%

['Good marks in Analog & Digital Electronics', 'Poor marks in Numerical Methods']=>['Good marks in Computer Architecture']: 90.0%

Relationship between: ("'Analog & Digital Electronics'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'")

['Good marks in Analog & Digital Electronics', 'Good marks in Numerical Methods']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

['Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods']=>['Good marks in Microprocessors & Microcontrollers']: 94.73684210526315%

['Good marks in Analog & Digital Electronics', 'Poor marks in Numerical Methods']=>['Good marks in Microprocessors & Microcontrollers']: 95.0%

Relationship between: ("'Analog & Digital Electronics'", "'Communication Engg & Coding Theory'", "'Computer Architecture'")

['Good marks in Analog & Digital Electronics', 'Good marks in Communication Engg & Coding Theory']=>['Good marks in Computer Architecture']: 95.83333333333334%

['Good marks in Analog & Digital Electronics', 'Average marks in Communication Engg & Coding Theory']=>['Good marks in Computer Architecture']: 94.44444444444444%

['Average marks in Analog & Digital Electronics', 'Average marks in Communication Engg & Coding Theory']=>['Average marks in Computer Architecture']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Communication Engg & Coding Theory'", "'Microprocessors & Microcontrollers'")

['Good marks in Analog & Digital Electronics', 'Good marks in Communication Engg & Coding Theory']=>['Good marks in Microprocessors & Microcontrollers']: 91.66666666666666%

['Good marks in Analog & Digital Electronics', 'Average marks in Communication Engg & Coding Theory']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Formal Language & Automata Theory'")

['Good marks in Analog & Digital Electronics', 'Good marks in Formal Language & Automata Theory']=>['Good marks in Computer Architecture']: 95.65217391304348%

['Good marks in Analog & Digital Electronics', 'Average marks in Formal Language & Automata Theory']=>['Good marks in Computer Architecture']: 95.65217391304348%

Relationship between: ("'Analog & Digital Electronics'", "'Formal Language & Automata Theory'", "'Microprocessors & Microcontrollers'")

['Good marks in Analog & Digital Electronics', 'Good marks in Formal Language & Automata Theory']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

['Good marks in Analog & Digital Electronics', 'Average marks in Formal Language & Automata Theory']=>['Good marks in Microprocessors & Microcontrollers']: 95.65217391304348%

Relationship between: ("'Analog & Digital Electronics'", "'Compiler Design'", "'Formal Language & Automata Theory'")

['Good marks in Analog & Digital Electronics', 'Good marks in Formal Language & Automata Theory']=>['Good marks in Compiler Design']: 95.65217391304348%

['Good marks in Analog & Digital Electronics', 'Average marks in Formal Language & Automata Theory']=>['Average marks in Compiler Design']: 91.30434782608695%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Microprocessors & Microcontrollers'")

['Good marks in Analog & Digital Electronics']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 92.85714285714286%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 96.29629629629629%

['Average marks in Analog & Digital Electronics']=>['Average marks in Computer Architecture', 'Average marks in Microprocessors & Microcontrollers']: 96.42857142857143%

['Average marks in Analog & Digital Electronics', 'Average marks in Computer Architecture']=>['Average marks in Microprocessors & Microcontrollers']: 96.42857142857143%

['Poor marks in Analog & Digital Electronics']=>['Poor marks in Computer Architecture', 'Poor marks in Microprocessors & Microcontrollers']: 85.71428571428571%

['Poor marks in Analog & Digital Electronics', 'Poor marks in Computer Architecture']=>['Poor marks in Microprocessors & Microcontrollers']: 92.3076923076923%

Relationship between: ("'Analog & Digital Electronics'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'")

['Good marks in Analog & Digital Electronics', 'Good marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

['Good marks in Analog & Digital Electronics', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 97.05882352941177%

Relationship between: ("'Analog & Digital Electronics'", "'Design & Analysis of Algorithm'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Good marks in Design & Analysis of Algorithm']=>['Good marks in Object Oriented Programming']: 100.0%

['Good marks in Analog & Digital Electronics', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

['Average marks in Analog & Digital Electronics', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Data Structure & Algorithm'")

['Good marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture']: 100.0%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture']: 96.7741935483871%

['Average marks in Data Structure & Algorithm', 'Average marks in Computer Organisation']=>['Average marks in Computer Architecture']: 100.0%

Relationship between: ("'Computer Organisation'", "'Data Structure & Algorithm'", "'Microprocessors & Microcontrollers'")

['Good marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Good marks in Microprocessors & Microcontrollers']: 94.11764705882352%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Numerical Methods'")

['Good marks in Data Structure & Algorithm']=>['Good marks in Numerical Methods', 'Good marks in Design & Analysis of Algorithm']: 80.0%

['Good marks in Data Structure & Algorithm', 'Good marks in Numerical Methods']=>['Good marks in Design & Analysis of Algorithm']: 100.0%

['Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

['Average marks in Data Structure & Algorithm', 'Poor marks in Numerical Methods']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

['Poor marks in Data Structure & Algorithm']=>['Poor marks in Numerical Methods', 'Poor marks in Design & Analysis of Algorithm']: 88.23529411764706%

['Poor marks in Data Structure & Algorithm', 'Poor marks in Numerical Methods']=>['Poor marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Data Structure & Algorithm'", "'Numerical Methods'", "'Object Oriented Programming'")

['Good marks in Data Structure & Algorithm']=>['Good marks in Numerical Methods', 'Good marks in Object Oriented Programming']: 80.0%

['Good marks in Data Structure & Algorithm', 'Good marks in Numerical Methods']=>['Good marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods']=>['Average marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Poor marks in Numerical Methods']=>['Average marks in Object Oriented Programming']: 100.0%

['Poor marks in Data Structure & Algorithm']=>['Poor marks in Numerical Methods', 'Poor marks in Object Oriented Programming']: 88.23529411764706%

['Poor marks in Data Structure & Algorithm', 'Poor marks in Numerical Methods']=>['Poor marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Formal Language & Automata Theory'")

['Good marks in Data Structure & Algorithm', 'Good marks in Formal Language & Automata Theory']=>['Good marks in Design & Analysis of Algorithm']: 100.0%

['Average marks in Data Structure & Algorithm', 'Good marks in Formal Language & Automata Theory']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

['Average marks in Data Structure & Algorithm', 'Average marks in Formal Language & Automata Theory']=>['Average marks in Design & Analysis of Algorithm']: 94.73684210526315%

Relationship between: ("'Data Structure & Algorithm'", "'Formal Language & Automata Theory'", "'Object Oriented Programming'")

['Good marks in Data Structure & Algorithm', 'Good marks in Formal Language & Automata Theory']=>['Good marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Good marks in Formal Language & Automata Theory']=>['Average marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Average marks in Formal Language & Automata Theory']=>['Average marks in Object Oriented Programming']: 94.73684210526315%

Relationship between: ("'Compiler Design'", "'Data Structure & Algorithm'", "'Formal Language & Automata Theory'")

['Good marks in Data Structure & Algorithm', 'Average marks in Formal Language & Automata Theory']=>['Average marks in Compiler Design']: 100.0%

['Average marks in Data Structure & Algorithm', 'Good marks in Formal Language & Automata Theory']=>['Good marks in Compiler Design']: 100.0%

['Average marks in Data Structure & Algorithm', 'Average marks in Formal Language & Automata Theory']=>['Average marks in Compiler Design']: 94.73684210526315%

Relationship between: ("'Computer Architecture'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'")

['Good marks in Data Structure & Algorithm', 'Good marks in Computer Architecture']=>['Good marks in Design & Analysis of Algorithm']: 78.94736842105263%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm']: 96.875%

['Average marks in Data Structure & Algorithm', 'Average marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Computer Architecture'", "'Data Structure & Algorithm'", "'Microprocessors & Microcontrollers'")

['Good marks in Data Structure & Algorithm', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 89.47368421052632%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 93.75%

Relationship between: ("'Computer Architecture'", "'Data Structure & Algorithm'", "'Object Oriented Programming'")

['Good marks in Data Structure & Algorithm', 'Good marks in Computer Architecture']=>['Good marks in Object Oriented Programming']: 78.94736842105263%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture']=>['Average marks in Object Oriented Programming']: 96.875%

['Average marks in Data Structure & Algorithm', 'Average marks in Computer Architecture']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Object Oriented Programming'")

['Good marks in Data Structure & Algorithm']=>['Good marks in Design & Analysis of Algorithm', 'Good marks in Object Oriented Programming']: 88.57142857142857%

['Good marks in Data Structure & Algorithm', 'Good marks in Design & Analysis of Algorithm']=>['Good marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 98.33333333333333%

['Average marks in Data Structure & Algorithm', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

['Poor marks in Data Structure & Algorithm']=>['Poor marks in Design & Analysis of Algorithm', 'Poor marks in Object Oriented Programming']: 100.0%

['Poor marks in Data Structure & Algorithm', 'Poor marks in Design & Analysis of Algorithm']=>['Poor marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Data Structure & Algorithm'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Good marks in Data Structure & Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Good marks in Object Oriented Programming']: 83.33333333333334%

['Average marks in Data Structure & Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 96.96969696969697%

['Average marks in Data Structure & Algorithm', 'Average marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Organisation'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'")

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Average marks in Design & Analysis of Algorithm']: 96.7741935483871%

['Average marks in Data Structure & Algorithm', 'Average marks in Computer Organisation']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Computer Organisation'", "'Data Structure & Algorithm'", "'Object Oriented Programming'")

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Average marks in Object Oriented Programming']: 96.7741935483871%

['Average marks in Data Structure & Algorithm', 'Average marks in Computer Organisation']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Communication Engg & Coding Theory'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'")

['Average marks in Data Structure & Algorithm', 'Good marks in Communication Engg & Coding Theory']=>['Average marks in Design & Analysis of Algorithm']: 95.65217391304348%

['Average marks in Data Structure & Algorithm', 'Average marks in Communication Engg & Coding Theory']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Communication Engg & Coding Theory'", "'Data Structure & Algorithm'", "'Object Oriented Programming'")

['Average marks in Data Structure & Algorithm', 'Good marks in Communication Engg & Coding Theory']=>['Average marks in Object Oriented Programming']: 95.65217391304348%

['Average marks in Data Structure & Algorithm', 'Average marks in Communication Engg & Coding Theory']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Data Structure & Algorithm'", "'Discrete Mathematics'", "'Object Oriented Programming'")

['Average marks in Data Structure & Algorithm', 'Good marks in Discrete Mathematics']=>['Average marks in Object Oriented Programming']: 95.23809523809523%

['Average marks in Data Structure & Algorithm', 'Average marks in Discrete Mathematics']=>['Average marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Poor marks in Discrete Mathematics']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Numerical Methods'")

['Good marks in Computer Organisation', 'Good marks in Numerical Methods']=>['Good marks in Computer Architecture']: 100.0%

['Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture']: 100.0%

['Good marks in Computer Organisation', 'Poor marks in Numerical Methods']=>['Good marks in Computer Architecture']: 85.71428571428571%

Relationship between: ("'Computer Organisation'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'")

['Good marks in Computer Organisation', 'Good marks in Numerical Methods']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

['Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

['Good marks in Computer Organisation', 'Poor marks in Numerical Methods']=>['Good marks in Microprocessors & Microcontrollers']: 90.47619047619048%

Relationship between: ("'Communication Engg & Coding Theory'", "'Computer Architecture'", "'Computer Organisation'")

['Good marks in Computer Organisation', 'Good marks in Communication Engg & Coding Theory']=>['Good marks in Computer Architecture']: 91.66666666666666%

['Good marks in Computer Organisation', 'Average marks in Communication Engg & Coding Theory']=>['Good marks in Computer Architecture']: 94.11764705882352%

['Average marks in Computer Organisation', 'Average marks in Communication Engg & Coding Theory']=>['Average marks in Computer Architecture']: 93.75%

Relationship between: ("'Communication Engg & Coding Theory'", "'Computer Organisation'", "'Microprocessors & Microcontrollers'")

['Good marks in Computer Organisation', 'Good marks in Communication Engg & Coding Theory']=>['Good marks in Microprocessors & Microcontrollers']: 91.66666666666666%

['Good marks in Computer Organisation', 'Average marks in Communication Engg & Coding Theory']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Formal Language & Automata Theory'")

['Good marks in Computer Organisation', 'Good marks in Formal Language & Automata Theory']=>['Good marks in Computer Architecture']: 95.45454545454545%

['Good marks in Computer Organisation', 'Average marks in Formal Language & Automata Theory']=>['Good marks in Computer Architecture']: 95.45454545454545%

Relationship between: ("'Computer Organisation'", "'Formal Language & Automata Theory'", "'Microprocessors & Microcontrollers'")

['Good marks in Computer Organisation', 'Good marks in Formal Language & Automata Theory']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

['Good marks in Computer Organisation', 'Average marks in Formal Language & Automata Theory']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Compiler Design'", "'Computer Organisation'", "'Formal Language & Automata Theory'")

['Good marks in Computer Organisation', 'Good marks in Formal Language & Automata Theory']=>['Good marks in Compiler Design']: 95.45454545454545%

['Good marks in Computer Organisation', 'Average marks in Formal Language & Automata Theory']=>['Average marks in Compiler Design']: 90.9090909090909%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Microprocessors & Microcontrollers'")

['Good marks in Computer Organisation']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 92.72727272727272%

['Good marks in Computer Organisation', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 98.07692307692307%

['Average marks in Computer Organisation']=>['Average marks in Computer Architecture', 'Average marks in Microprocessors & Microcontrollers']: 93.10344827586206%

['Average marks in Computer Organisation', 'Average marks in Computer Architecture']=>['Average marks in Microprocessors & Microcontrollers']: 96.42857142857143%

['Poor marks in Computer Organisation']=>['Poor marks in Computer Architecture', 'Poor marks in Microprocessors & Microcontrollers']: 82.14285714285714%

['Poor marks in Computer Organisation', 'Poor marks in Computer Architecture']=>['Poor marks in Microprocessors & Microcontrollers']: 92.0%

Relationship between: ("'Computer Organisation'", "'Design & Analysis of Algorithm'", "'Object Oriented Programming'")

['Good marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

['Average marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Compiler Design'", "'Formal Language & Automata Theory'", "'Numerical Methods'")

['Good marks in Numerical Methods', 'Good marks in Formal Language & Automata Theory']=>['Good marks in Compiler Design']: 93.75%

['Average marks in Numerical Methods', 'Good marks in Formal Language & Automata Theory']=>['Good marks in Compiler Design']: 100.0%

Relationship between: ("'Computer Architecture'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'")

['Good marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 94.44444444444444%

['Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 90.0%

['Poor marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 94.44444444444444%

Relationship between: ("'Design & Analysis of Algorithm'", "'Numerical Methods'", "'Object Oriented Programming'")

['Good marks in Numerical Methods']=>['Good marks in Design & Analysis of Algorithm', 'Good marks in Object Oriented Programming']: 85.29411764705883%

['Good marks in Numerical Methods', 'Good marks in Design & Analysis of Algorithm']=>['Good marks in Object Oriented Programming']: 100.0%

['Average marks in Numerical Methods']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 97.43589743589743%

['Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

['Poor marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

['Poor marks in Numerical Methods', 'Poor marks in Design & Analysis of Algorithm']=>['Poor marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Communication Engg & Coding Theory'", "'Compiler Design'", "'Formal Language & Automata Theory'")

['Good marks in Communication Engg & Coding Theory', 'Good marks in Formal Language & Automata Theory']=>['Good marks in Compiler Design']: 100.0%

['Good marks in Communication Engg & Coding Theory', 'Average marks in Formal Language & Automata Theory']=>['Average marks in Compiler Design']: 95.23809523809523%

['Average marks in Communication Engg & Coding Theory', 'Good marks in Formal Language & Automata Theory']=>['Good marks in Compiler Design']: 85.71428571428571%

Relationship between: ("'Communication Engg & Coding Theory'", "'Computer Architecture'", "'Microprocessors & Microcontrollers'")

['Good marks in Communication Engg & Coding Theory', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 87.5%

['Average marks in Communication Engg & Coding Theory', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 94.44444444444444%

Relationship between: ("'Communication Engg & Coding Theory'", "'Design & Analysis of Algorithm'", "'Object Oriented Programming'")

['Good marks in Communication Engg & Coding Theory', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

['Average marks in Communication Engg & Coding Theory', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Architecture'", "'Formal Language & Automata Theory'", "'Microprocessors & Microcontrollers'")

['Good marks in Formal Language & Automata Theory', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 91.66666666666666%

['Average marks in Formal Language & Automata Theory', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 95.45454545454545%

Relationship between: ("'Compiler Design'", "'Computer Architecture'", "'Formal Language & Automata Theory'")

['Good marks in Formal Language & Automata Theory', 'Good marks in Computer Architecture']=>['Good marks in Compiler Design']: 95.83333333333334%

['Average marks in Formal Language & Automata Theory', 'Good marks in Computer Architecture']=>['Average marks in Compiler Design']: 90.9090909090909%

Relationship between: ("'Design & Analysis of Algorithm'", "'Formal Language & Automata Theory'", "'Object Oriented Programming'")

['Good marks in Formal Language & Automata Theory', 'Good marks in Design & Analysis of Algorithm']=>['Good marks in Object Oriented Programming']: 100.0%

['Good marks in Formal Language & Automata Theory', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

['Average marks in Formal Language & Automata Theory', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Compiler Design'", "'Design & Analysis of Algorithm'", "'Formal Language & Automata Theory'")

['Good marks in Formal Language & Automata Theory', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Compiler Design']: 100.0%

['Average marks in Formal Language & Automata Theory', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Compiler Design']: 95.23809523809523%

Relationship between: ("'Compiler Design'", "'Formal Language & Automata Theory'", "'Microprocessors & Microcontrollers'")

['Good marks in Formal Language & Automata Theory', 'Good marks in Microprocessors & Microcontrollers']=>['Good marks in Compiler Design']: 92.0%

['Average marks in Formal Language & Automata Theory', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Compiler Design']: 90.9090909090909%

Relationship between: ("'Compiler Design'", "'Discrete Mathematics'", "'Formal Language & Automata Theory'")

['Good marks in Formal Language & Automata Theory', 'Good marks in Discrete Mathematics']=>['Good marks in Compiler Design']: 100.0%

['Good marks in Formal Language & Automata Theory', 'Poor marks in Discrete Mathematics']=>['Good marks in Compiler Design']: 90.9090909090909%

['Average marks in Formal Language & Automata Theory', 'Average marks in Discrete Mathematics']=>['Average marks in Compiler Design']: 100.0%

Relationship between: ("'Compiler Design'", "'Formal Language & Automata Theory'", "'Object Oriented Programming'")

['Good marks in Formal Language & Automata Theory', 'Average marks in Object Oriented Programming']=>['Good marks in Compiler Design']: 100.0%

['Average marks in Formal Language & Automata Theory', 'Average marks in Object Oriented Programming']=>['Average marks in Compiler Design']: 95.23809523809523%

Relationship between: ("'Compiler Design'", "'Data Base Management System'", "'Formal Language & Automata Theory'")

['Good marks in Formal Language & Automata Theory', 'Average marks in Data Base Management System']=>['Good marks in Compiler Design']: 95.0%

['Good marks in Formal Language & Automata Theory', 'Poor marks in Data Base Management System']=>['Good marks in Compiler Design']: 100.0%

['Average marks in Formal Language & Automata Theory', 'Good marks in Data Base Management System']=>['Average marks in Compiler Design']: 94.11764705882352%

Relationship between: ("'Compiler Design'", "'Computer Networks'", "'Formal Language & Automata Theory'")

['Good marks in Formal Language & Automata Theory', 'Good marks in Computer Networks']=>['Good marks in Compiler Design']: 94.44444444444444%

['Good marks in Formal Language & Automata Theory', 'Poor marks in Computer Networks']=>['Good marks in Compiler Design']: 93.75%

['Average marks in Formal Language & Automata Theory', 'Good marks in Computer Networks']=>['Average marks in Compiler Design']: 100.0%

Relationship between: ("'Compiler Design'", "'Formal Language & Automata Theory'", "'Operating System'")

['Good marks in Formal Language & Automata Theory', 'Average marks in Operating System']=>['Good marks in Compiler Design']: 89.47368421052632%

['Good marks in Formal Language & Automata Theory', 'Poor marks in Operating System']=>['Good marks in Compiler Design']: 94.44444444444444%

['Average marks in Formal Language & Automata Theory', 'Average marks in Operating System']=>['Average marks in Compiler Design']: 91.30434782608695%

Relationship between: ("'Compiler Design'", "'Formal Language & Automata Theory'", "'Information Theory & Coding / Computer Graphics'")

['Good marks in Formal Language & Automata Theory', 'Average marks in Information Theory & Coding / Computer Graphics']=>['Good marks in Compiler Design']: 95.0%

['Good marks in Formal Language & Automata Theory', 'Poor marks in Information Theory & Coding / Computer Graphics']=>['Good marks in Compiler Design']: 100.0%

['Average marks in Formal Language & Automata Theory', 'Good marks in Information Theory & Coding / Computer Graphics']=>['Average marks in Compiler Design']: 100.0%

['Average marks in Formal Language & Automata Theory', 'Poor marks in Information Theory & Coding / Computer Graphics']=>['Average marks in Compiler Design']: 88.23529411764706%

Relationship between: ("'Compiler Design'", "'Formal Language & Automata Theory'", "'Operation Research'")

['Good marks in Formal Language & Automata Theory', 'Average marks in Operation Research']=>['Good marks in Compiler Design']: 90.0%

['Good marks in Formal Language & Automata Theory', 'Poor marks in Operation Research']=>['Good marks in Compiler Design']: 100.0%

['Average marks in Formal Language & Automata Theory', 'Average marks in Operation Research']=>['Average marks in Compiler Design']: 93.75%

Relationship between: ("'Computer Architecture'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'")

['Good marks in Computer Architecture', 'Good marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 93.75%

['Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 94.11764705882352%

Relationship between: ("'Computer Architecture'", "'Design & Analysis of Algorithm'", "'Object Oriented Programming'")

['Good marks in Computer Architecture', 'Good marks in Design & Analysis of Algorithm']=>['Good marks in Object Oriented Programming']: 100.0%

['Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

['Average marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Good marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Good marks in Object Oriented Programming']: 100.0%

['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

['Average marks in Design & Analysis of Algorithm', 'Average marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Design & Analysis of Algorithm'", "'Discrete Mathematics'", "'Object Oriented Programming'")

['Average marks in Design & Analysis of Algorithm', 'Good marks in Discrete Mathematics']=>['Average marks in Object Oriented Programming']: 100.0%

['Average marks in Design & Analysis of Algorithm', 'Average marks in Discrete Mathematics']=>['Average marks in Object Oriented Programming']: 100.0%

['Average marks in Design & Analysis of Algorithm', 'Poor marks in Discrete Mathematics']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Data Structure & Algorithm'", "'Introduction to Computing'")

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Data Structure & Algorithm', 'Good marks in Computer Architecture']: 83.33333333333334%

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Data Structure & Algorithm']=>['Good marks in Computer Architecture']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture']: 90.47619047619048%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Good marks in Computer Architecture']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Computer Organisation'", "'Introduction to Computing'")

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Organisation', 'Good marks in Computer Architecture']: 94.44444444444444%

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Organisation', 'Good marks in Computer Architecture']: 95.23809523809523%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture']: 100.0%

['Poor marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Organisation', 'Good marks in Computer Architecture']: 88.23529411764706%

['Poor marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture']: 88.23529411764706%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Organisation'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'")

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Organisation', 'Good marks in Microprocessors & Microcontrollers']: 88.88888888888889%

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation']=>['Good marks in Microprocessors & Microcontrollers']: 94.11764705882352%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Organisation', 'Good marks in Microprocessors & Microcontrollers']: 95.23809523809523%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

['Poor marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Organisation', 'Good marks in Microprocessors & Microcontrollers']: 100.0%

['Poor marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Introduction to Computing'", "'Numerical Methods'")

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Numerical Methods', 'Good marks in Computer Architecture']: 83.33333333333334%

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Numerical Methods']=>['Good marks in Computer Architecture']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Numerical Methods', 'Good marks in Computer Architecture']: 90.47619047619048%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'")

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Numerical Methods', 'Good marks in Microprocessors & Microcontrollers']: 83.33333333333334%

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Numerical Methods']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Numerical Methods', 'Good marks in Microprocessors & Microcontrollers']: 85.71428571428571%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods']=>['Good marks in Microprocessors & Microcontrollers']: 94.73684210526315%

['Poor marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Poor marks in Numerical Methods', 'Good marks in Microprocessors & Microcontrollers']: 94.11764705882352%

['Poor marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Poor marks in Numerical Methods']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'")

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Architecture', 'Good marks in Design & Analysis of Algorithm']: 83.33333333333334%

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Architecture']=>['Good marks in Design & Analysis of Algorithm']: 83.33333333333334%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'")

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 94.44444444444444%

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 94.44444444444444%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 95.23809523809523%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 95.23809523809523%

['Poor marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 88.23529411764706%

['Poor marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Introduction to Computing'", "'Object Oriented Programming'")

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Architecture', 'Good marks in Object Oriented Programming']: 83.33333333333334%

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Architecture']=>['Good marks in Object Oriented Programming']: 83.33333333333334%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Architecture', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Architecture']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'")

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 83.33333333333334%

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 95.23809523809523%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 95.23809523809523%

Relationship between: ("'Analog & Digital Electronics'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Object Oriented Programming'")

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Design & Analysis of Algorithm', 'Good marks in Object Oriented Programming']: 83.33333333333334%

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Design & Analysis of Algorithm']=>['Good marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Microprocessors & Microcontrollers', 'Good marks in Object Oriented Programming']: 83.33333333333334%

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Microprocessors & Microcontrollers']=>['Good marks in Object Oriented Programming']: 88.23529411764706%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 95.23809523809523%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Numerical Methods'")

['Good marks in Introduction to Computing']=>['Good marks in Data Structure & Algorithm', 'Good marks in Numerical Methods', 'Good marks in Design & Analysis of Algorithm']: 76.47058823529412%

['Good marks in Introduction to Computing', 'Good marks in Data Structure & Algorithm']=>['Good marks in Numerical Methods', 'Good marks in Design & Analysis of Algorithm']: 89.65517241379311%

['Good marks in Introduction to Computing', 'Good marks in Data Structure & Algorithm', 'Good marks in Numerical Methods']=>['Good marks in Design & Analysis of Algorithm']: 100.0%

['Average marks in Introduction to Computing']=>['Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']: 85.0%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm']=>['Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']: 94.44444444444444%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

['Poor marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm']=>['Poor marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']: 94.73684210526315%

['Poor marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Poor marks in Numerical Methods']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

['Poor marks in Introduction to Computing', 'Poor marks in Data Structure & Algorithm']=>['Poor marks in Numerical Methods', 'Poor marks in Design & Analysis of Algorithm']: 88.23529411764706%

['Poor marks in Introduction to Computing', 'Poor marks in Data Structure & Algorithm', 'Poor marks in Numerical Methods']=>['Poor marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Data Structure & Algorithm'", "'Introduction to Computing'", "'Numerical Methods'", "'Object Oriented Programming'")

['Good marks in Introduction to Computing']=>['Good marks in Data Structure & Algorithm', 'Good marks in Numerical Methods', 'Good marks in Object Oriented Programming']: 76.47058823529412%

['Good marks in Introduction to Computing', 'Good marks in Data Structure & Algorithm']=>['Good marks in Numerical Methods', 'Good marks in Object Oriented Programming']: 89.65517241379311%

['Good marks in Introduction to Computing', 'Good marks in Data Structure & Algorithm', 'Good marks in Numerical Methods']=>['Good marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing']=>['Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods', 'Average marks in Object Oriented Programming']: 85.0%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm']=>['Average marks in Numerical Methods', 'Average marks in Object Oriented Programming']: 94.44444444444444%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods']=>['Average marks in Object Oriented Programming']: 100.0%

['Poor marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm']=>['Poor marks in Numerical Methods', 'Average marks in Object Oriented Programming']: 94.73684210526315%

['Poor marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Poor marks in Numerical Methods']=>['Average marks in Object Oriented Programming']: 100.0%

['Poor marks in Introduction to Computing', 'Poor marks in Data Structure & Algorithm']=>['Poor marks in Numerical Methods', 'Poor marks in Object Oriented Programming']: 88.23529411764706%

['Poor marks in Introduction to Computing', 'Poor marks in Data Structure & Algorithm', 'Poor marks in Numerical Methods']=>['Poor marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Architecture'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'")

['Good marks in Introduction to Computing', 'Good marks in Data Structure & Algorithm', 'Good marks in Computer Architecture']=>['Good marks in Design & Analysis of Algorithm']: 93.75%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Computer Architecture'", "'Data Structure & Algorithm'", "'Introduction to Computing'", "'Object Oriented Programming'")

['Good marks in Introduction to Computing', 'Good marks in Data Structure & Algorithm', 'Good marks in Computer Architecture']=>['Good marks in Object Oriented Programming']: 93.75%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Object Oriented Programming'")

['Good marks in Introduction to Computing']=>['Good marks in Data Structure & Algorithm', 'Good marks in Design & Analysis of Algorithm', 'Good marks in Object Oriented Programming']: 82.35294117647058%

['Good marks in Introduction to Computing', 'Good marks in Data Structure & Algorithm']=>['Good marks in Design & Analysis of Algorithm', 'Good marks in Object Oriented Programming']: 96.55172413793103%

['Good marks in Introduction to Computing', 'Good marks in Data Structure & Algorithm', 'Good marks in Design & Analysis of Algorithm']=>['Good marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing']=>['Average marks in Data Structure & Algorithm', 'Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 90.0%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

['Poor marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Poor marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

['Poor marks in Introduction to Computing', 'Poor marks in Data Structure & Algorithm']=>['Poor marks in Design & Analysis of Algorithm', 'Poor marks in Object Oriented Programming']: 100.0%

['Poor marks in Introduction to Computing', 'Poor marks in Data Structure & Algorithm', 'Poor marks in Design & Analysis of Algorithm']=>['Poor marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Data Structure & Algorithm'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Good marks in Introduction to Computing', 'Good marks in Data Structure & Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Good marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'")

['Good marks in Introduction to Computing', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 94.11764705882352%

['Good marks in Introduction to Computing', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 94.11764705882352%

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

['Poor marks in Introduction to Computing', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 83.33333333333334%

['Poor marks in Introduction to Computing', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Computer Architecture'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'")

['Good marks in Introduction to Computing', 'Good marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 93.75%

['Average marks in Introduction to Computing', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 90.0%

Relationship between: ("'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Numerical Methods'", "'Object Oriented Programming'")

['Good marks in Introduction to Computing']=>['Good marks in Numerical Methods', 'Good marks in Design & Analysis of Algorithm', 'Good marks in Object Oriented Programming']: 79.41176470588235%

['Good marks in Introduction to Computing', 'Good marks in Numerical Methods']=>['Good marks in Design & Analysis of Algorithm', 'Good marks in Object Oriented Programming']: 93.10344827586206%

['Good marks in Introduction to Computing', 'Good marks in Numerical Methods', 'Good marks in Design & Analysis of Algorithm']=>['Good marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing']=>['Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 90.0%

['Average marks in Introduction to Computing', 'Average marks in Numerical Methods']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 97.2972972972973%

['Average marks in Introduction to Computing', 'Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

['Poor marks in Introduction to Computing', 'Poor marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

['Poor marks in Introduction to Computing', 'Poor marks in Numerical Methods', 'Poor marks in Design & Analysis of Algorithm']=>['Poor marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Architecture'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'")

['Good marks in Introduction to Computing', 'Good marks in Computer Architecture']=>['Good marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 78.94736842105263%

['Good marks in Introduction to Computing', 'Good marks in Computer Architecture', 'Good marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 93.75%

['Average marks in Introduction to Computing', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 90.9090909090909%

['Average marks in Introduction to Computing', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 90.9090909090909%

Relationship between: ("'Computer Architecture'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Object Oriented Programming'")

['Good marks in Introduction to Computing', 'Good marks in Computer Architecture']=>['Good marks in Design & Analysis of Algorithm', 'Good marks in Object Oriented Programming']: 84.21052631578947%

['Good marks in Introduction to Computing', 'Good marks in Computer Architecture', 'Good marks in Design & Analysis of Algorithm']=>['Good marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Architecture'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Good marks in Introduction to Computing', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers', 'Good marks in Object Oriented Programming']: 78.94736842105263%

['Good marks in Introduction to Computing', 'Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']=>['Good marks in Object Oriented Programming']: 88.23529411764706%

['Average marks in Introduction to Computing', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 90.9090909090909%

['Average marks in Introduction to Computing', 'Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Good marks in Introduction to Computing', 'Good marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Good marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Organisation'", "'Data Structure & Algorithm'", "'Introduction to Computing'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']: 85.71428571428571%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Good marks in Computer Organisation']: 94.73684210526315%

Relationship between: ("'Analog & Digital Electronics'", "'Data Structure & Algorithm'", "'Introduction to Computing'", "'Numerical Methods'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods']: 80.95238095238095%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Average marks in Numerical Methods']: 89.47368421052632%

Relationship between: ("'Analog & Digital Electronics'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Data Structure & Algorithm', 'Average marks in Design & Analysis of Algorithm']: 90.47619047619048%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Data Structure & Algorithm'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Data Structure & Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 85.71428571428571%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 94.73684210526315%

Relationship between: ("'Analog & Digital Electronics'", "'Data Structure & Algorithm'", "'Introduction to Computing'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Data Structure & Algorithm', 'Average marks in Object Oriented Programming']: 90.47619047619048%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Organisation'", "'Introduction to Computing'", "'Numerical Methods'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Organisation', 'Average marks in Numerical Methods']: 85.71428571428571%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation']=>['Average marks in Numerical Methods']: 90.0%

['Poor marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Organisation', 'Poor marks in Numerical Methods']: 94.11764705882352%

['Poor marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation']=>['Poor marks in Numerical Methods']: 94.11764705882352%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Organisation'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm']: 95.23809523809523%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Organisation'", "'Introduction to Computing'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Organisation', 'Average marks in Object Oriented Programming']: 95.23809523809523%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Numerical Methods'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']: 90.47619047619048%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Introduction to Computing'", "'Numerical Methods'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Numerical Methods', 'Average marks in Object Oriented Programming']: 90.47619047619048%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Introduction to Computing'", "'Numerical Methods'")

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation']=>['Average marks in Numerical Methods', 'Good marks in Computer Architecture']: 90.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture']: 100.0%

Relationship between: ("'Computer Organisation'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Numerical Methods'")

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation']=>['Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']: 90.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Computer Organisation'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'")

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation']=>['Average marks in Numerical Methods', 'Good marks in Microprocessors & Microcontrollers']: 90.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

['Poor marks in Introduction to Computing', 'Good marks in Computer Organisation']=>['Poor marks in Numerical Methods', 'Good marks in Microprocessors & Microcontrollers']: 88.88888888888889%

['Poor marks in Introduction to Computing', 'Good marks in Computer Organisation', 'Poor marks in Numerical Methods']=>['Good marks in Microprocessors & Microcontrollers']: 94.11764705882352%

Relationship between: ("'Computer Organisation'", "'Introduction to Computing'", "'Numerical Methods'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation']=>['Average marks in Numerical Methods', 'Average marks in Object Oriented Programming']: 90.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'")

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Introduction to Computing'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Organisation'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'")

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Computer Organisation'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Organisation'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Communication Engg & Coding Theory'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Average marks in Communication Engg & Coding Theory']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 94.11764705882352%

['Average marks in Introduction to Computing', 'Average marks in Communication Engg & Coding Theory', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Design & Analysis of Algorithm'", "'Formal Language & Automata Theory'", "'Introduction to Computing'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Good marks in Formal Language & Automata Theory']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 94.73684210526315%

['Average marks in Introduction to Computing', 'Good marks in Formal Language & Automata Theory', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Compiler Design'", "'Design & Analysis of Algorithm'", "'Formal Language & Automata Theory'", "'Introduction to Computing'")

['Average marks in Introduction to Computing', 'Good marks in Formal Language & Automata Theory']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Compiler Design']: 94.73684210526315%

['Average marks in Introduction to Computing', 'Good marks in Formal Language & Automata Theory', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Compiler Design']: 100.0%

Relationship between: ("'Compiler Design'", "'Formal Language & Automata Theory'", "'Introduction to Computing'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Good marks in Formal Language & Automata Theory']=>['Average marks in Object Oriented Programming', 'Good marks in Compiler Design']: 94.73684210526315%

['Average marks in Introduction to Computing', 'Good marks in Formal Language & Automata Theory', 'Average marks in Object Oriented Programming']=>['Good marks in Compiler Design']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Computer Organisation'", "'Data Structure & Algorithm'")

['Good marks in Analog & Digital Electronics', 'Good marks in Data Structure & Algorithm']=>['Good marks in Computer Organisation', 'Good marks in Computer Architecture']: 94.44444444444444%

['Good marks in Analog & Digital Electronics', 'Good marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture']: 100.0%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Good marks in Computer Organisation', 'Good marks in Computer Architecture']: 93.75%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture']: 96.7741935483871%

['Average marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Average marks in Computer Organisation', 'Average marks in Computer Architecture']: 100.0%

['Average marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Computer Organisation']=>['Average marks in Computer Architecture']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Organisation'", "'Data Structure & Algorithm'", "'Microprocessors & Microcontrollers'")

['Good marks in Analog & Digital Electronics', 'Good marks in Data Structure & Algorithm']=>['Good marks in Computer Organisation', 'Good marks in Microprocessors & Microcontrollers']: 88.88888888888889%

['Good marks in Analog & Digital Electronics', 'Good marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Good marks in Microprocessors & Microcontrollers']: 94.11764705882352%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Good marks in Computer Organisation', 'Good marks in Microprocessors & Microcontrollers']: 96.875%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Data Structure & Algorithm'", "'Microprocessors & Microcontrollers'")

['Good marks in Analog & Digital Electronics', 'Good marks in Data Structure & Algorithm']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 94.44444444444444%

['Good marks in Analog & Digital Electronics', 'Good marks in Data Structure & Algorithm', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 94.44444444444444%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 93.75%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 96.7741935483871%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Organisation'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'")

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Good marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm']: 93.75%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Average marks in Design & Analysis of Algorithm']: 96.7741935483871%

['Average marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Average marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm']: 100.0%

['Average marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Computer Organisation']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Organisation'", "'Data Structure & Algorithm'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Good marks in Computer Organisation', 'Average marks in Object Oriented Programming']: 93.75%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Average marks in Object Oriented Programming']: 96.7741935483871%

['Average marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Average marks in Computer Organisation', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Computer Organisation']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'")

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']: 93.75%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm']: 96.7741935483871%

['Average marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Average marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']: 100.0%

['Average marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Data Structure & Algorithm'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Good marks in Computer Architecture', 'Average marks in Object Oriented Programming']: 93.75%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture']=>['Average marks in Object Oriented Programming']: 96.7741935483871%

['Average marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Average marks in Computer Architecture', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Computer Architecture']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'")

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 93.75%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 96.7741935483871%

Relationship between: ("'Analog & Digital Electronics'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 96.875%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

['Average marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Data Structure & Algorithm'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 93.75%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 96.7741935483871%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Computer Organisation'", "'Numerical Methods'")

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Good marks in Numerical Methods']=>['Good marks in Computer Architecture']: 100.0%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture']: 100.0%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Poor marks in Numerical Methods']=>['Good marks in Computer Architecture']: 90.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Organisation'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'")

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Good marks in Numerical Methods']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Poor marks in Numerical Methods']=>['Good marks in Microprocessors & Microcontrollers']: 95.0%

Relationship between: ("'Analog & Digital Electronics'", "'Communication Engg & Coding Theory'", "'Computer Architecture'", "'Computer Organisation'")

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Good marks in Communication Engg & Coding Theory']=>['Good marks in Computer Architecture']: 95.65217391304348%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Communication Engg & Coding Theory']=>['Good marks in Computer Architecture']: 94.11764705882352%

['Average marks in Analog & Digital Electronics', 'Average marks in Computer Organisation', 'Average marks in Communication Engg & Coding Theory']=>['Average marks in Computer Architecture']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Communication Engg & Coding Theory'", "'Computer Organisation'", "'Microprocessors & Microcontrollers'")

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Good marks in Communication Engg & Coding Theory']=>['Good marks in Microprocessors & Microcontrollers']: 95.65217391304348%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Communication Engg & Coding Theory']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Computer Organisation'", "'Formal Language & Automata Theory'")

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Good marks in Formal Language & Automata Theory']=>['Good marks in Computer Architecture']: 95.45454545454545%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Formal Language & Automata Theory']=>['Good marks in Computer Architecture']: 95.45454545454545%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Organisation'", "'Formal Language & Automata Theory'", "'Microprocessors & Microcontrollers'")

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Good marks in Formal Language & Automata Theory']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Formal Language & Automata Theory']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Compiler Design'", "'Computer Organisation'", "'Formal Language & Automata Theory'")

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Good marks in Formal Language & Automata Theory']=>['Good marks in Compiler Design']: 95.45454545454545%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Formal Language & Automata Theory']=>['Average marks in Compiler Design']: 90.9090909090909%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Computer Organisation'", "'Microprocessors & Microcontrollers'")

['Good marks in Analog & Digital Electronics']=>['Good marks in Computer Organisation', 'Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 91.07142857142857%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 94.44444444444444%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 98.07692307692307%

['Average marks in Analog & Digital Electronics']=>['Average marks in Computer Organisation', 'Average marks in Computer Architecture', 'Average marks in Microprocessors & Microcontrollers']: 96.42857142857143%

['Average marks in Analog & Digital Electronics', 'Average marks in Computer Organisation']=>['Average marks in Computer Architecture', 'Average marks in Microprocessors & Microcontrollers']: 96.42857142857143%

['Average marks in Analog & Digital Electronics', 'Average marks in Computer Organisation', 'Average marks in Computer Architecture']=>['Average marks in Microprocessors & Microcontrollers']: 96.42857142857143%

['Poor marks in Analog & Digital Electronics']=>['Poor marks in Computer Organisation', 'Poor marks in Computer Architecture', 'Poor marks in Microprocessors & Microcontrollers']: 82.14285714285714%

['Poor marks in Analog & Digital Electronics', 'Poor marks in Computer Organisation']=>['Poor marks in Computer Architecture', 'Poor marks in Microprocessors & Microcontrollers']: 85.18518518518519%

['Poor marks in Analog & Digital Electronics', 'Poor marks in Computer Organisation', 'Poor marks in Computer Architecture']=>['Poor marks in Microprocessors & Microcontrollers']: 92.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Organisation'", "'Design & Analysis of Algorithm'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

['Average marks in Analog & Digital Electronics', 'Average marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'")

['Good marks in Analog & Digital Electronics', 'Good marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 100.0%

['Good marks in Analog & Digital Electronics', 'Good marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

['Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 94.73684210526315%

['Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 94.73684210526315%

['Good marks in Analog & Digital Electronics', 'Poor marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 85.0%

['Good marks in Analog & Digital Electronics', 'Poor marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 94.44444444444444%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Design & Analysis of Algorithm'", "'Numerical Methods'")

['Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']: 100.0%

['Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Numerical Methods'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Average marks in Object Oriented Programming']: 100.0%

['Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'")

['Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 94.73684210526315%

['Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 94.73684210526315%

Relationship between: ("'Analog & Digital Electronics'", "'Design & Analysis of Algorithm'", "'Numerical Methods'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 94.73684210526315%

['Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Communication Engg & Coding Theory'", "'Computer Architecture'", "'Microprocessors & Microcontrollers'")

['Good marks in Analog & Digital Electronics', 'Good marks in Communication Engg & Coding Theory']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 87.5%

['Good marks in Analog & Digital Electronics', 'Good marks in Communication Engg & Coding Theory', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 91.30434782608695%

['Good marks in Analog & Digital Electronics', 'Average marks in Communication Engg & Coding Theory']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 94.44444444444444%

['Good marks in Analog & Digital Electronics', 'Average marks in Communication Engg & Coding Theory', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Formal Language & Automata Theory'", "'Microprocessors & Microcontrollers'")

['Good marks in Analog & Digital Electronics', 'Good marks in Formal Language & Automata Theory']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 95.65217391304348%

['Good marks in Analog & Digital Electronics', 'Good marks in Formal Language & Automata Theory', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

['Good marks in Analog & Digital Electronics', 'Average marks in Formal Language & Automata Theory']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 91.30434782608695%

['Good marks in Analog & Digital Electronics', 'Average marks in Formal Language & Automata Theory', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 95.45454545454545%

Relationship between: ("'Analog & Digital Electronics'", "'Compiler Design'", "'Computer Architecture'", "'Formal Language & Automata Theory'")

['Good marks in Analog & Digital Electronics', 'Good marks in Formal Language & Automata Theory']=>['Good marks in Computer Architecture', 'Good marks in Compiler Design']: 91.30434782608695%

['Good marks in Analog & Digital Electronics', 'Good marks in Formal Language & Automata Theory', 'Good marks in Computer Architecture']=>['Good marks in Compiler Design']: 95.45454545454545%

['Good marks in Analog & Digital Electronics', 'Average marks in Formal Language & Automata Theory']=>['Good marks in Computer Architecture', 'Average marks in Compiler Design']: 86.95652173913044%

['Good marks in Analog & Digital Electronics', 'Average marks in Formal Language & Automata Theory', 'Good marks in Computer Architecture']=>['Average marks in Compiler Design']: 90.9090909090909%

Relationship between: ("'Analog & Digital Electronics'", "'Compiler Design'", "'Formal Language & Automata Theory'", "'Microprocessors & Microcontrollers'")

['Good marks in Analog & Digital Electronics', 'Good marks in Formal Language & Automata Theory']=>['Good marks in Microprocessors & Microcontrollers', 'Good marks in Compiler Design']: 95.65217391304348%

['Good marks in Analog & Digital Electronics', 'Good marks in Formal Language & Automata Theory', 'Good marks in Microprocessors & Microcontrollers']=>['Good marks in Compiler Design']: 95.65217391304348%

['Good marks in Analog & Digital Electronics', 'Average marks in Formal Language & Automata Theory']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Compiler Design']: 86.95652173913044%

['Good marks in Analog & Digital Electronics', 'Average marks in Formal Language & Automata Theory', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Compiler Design']: 90.9090909090909%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'")

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Architecture', 'Good marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 96.96969696969697%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Design & Analysis of Algorithm'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Architecture', 'Good marks in Design & Analysis of Algorithm']=>['Good marks in Object Oriented Programming']: 100.0%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

['Average marks in Analog & Digital Electronics', 'Average marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Good marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers', 'Good marks in Object Oriented Programming']: 100.0%

['Good marks in Analog & Digital Electronics', 'Good marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Good marks in Object Oriented Programming']: 100.0%

['Good marks in Analog & Digital Electronics', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 97.05882352941177%

['Good marks in Analog & Digital Electronics', 'Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Data Structure & Algorithm'", "'Microprocessors & Microcontrollers'")

['Good marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 94.11764705882352%

['Good marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 94.11764705882352%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 96.7741935483871%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Numerical Methods'", "'Object Oriented Programming'")

['Good marks in Data Structure & Algorithm']=>['Good marks in Numerical Methods', 'Good marks in Design & Analysis of Algorithm', 'Good marks in Object Oriented Programming']: 80.0%

['Good marks in Data Structure & Algorithm', 'Good marks in Numerical Methods']=>['Good marks in Design & Analysis of Algorithm', 'Good marks in Object Oriented Programming']: 100.0%

['Good marks in Data Structure & Algorithm', 'Good marks in Numerical Methods', 'Good marks in Design & Analysis of Algorithm']=>['Good marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Poor marks in Numerical Methods']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Poor marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

['Poor marks in Data Structure & Algorithm']=>['Poor marks in Numerical Methods', 'Poor marks in Design & Analysis of Algorithm', 'Poor marks in Object Oriented Programming']: 88.23529411764706%

['Poor marks in Data Structure & Algorithm', 'Poor marks in Numerical Methods']=>['Poor marks in Design & Analysis of Algorithm', 'Poor marks in Object Oriented Programming']: 100.0%

['Poor marks in Data Structure & Algorithm', 'Poor marks in Numerical Methods', 'Poor marks in Design & Analysis of Algorithm']=>['Poor marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Formal Language & Automata Theory'", "'Object Oriented Programming'")

['Good marks in Data Structure & Algorithm', 'Good marks in Formal Language & Automata Theory']=>['Good marks in Design & Analysis of Algorithm', 'Good marks in Object Oriented Programming']: 100.0%

['Good marks in Data Structure & Algorithm', 'Good marks in Formal Language & Automata Theory', 'Good marks in Design & Analysis of Algorithm']=>['Good marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Good marks in Formal Language & Automata Theory']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Good marks in Formal Language & Automata Theory', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Average marks in Formal Language & Automata Theory']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 94.73684210526315%

['Average marks in Data Structure & Algorithm', 'Average marks in Formal Language & Automata Theory', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Architecture'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Object Oriented Programming'")

['Good marks in Data Structure & Algorithm', 'Good marks in Computer Architecture']=>['Good marks in Design & Analysis of Algorithm', 'Good marks in Object Oriented Programming']: 78.94736842105263%

['Good marks in Data Structure & Algorithm', 'Good marks in Computer Architecture', 'Good marks in Design & Analysis of Algorithm']=>['Good marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 96.875%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Average marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Average marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Good marks in Data Structure & Algorithm', 'Good marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Good marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Average marks in Design & Analysis of Algorithm', 'Average marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'")

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']: 93.54838709677419%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm']: 96.66666666666667%

['Average marks in Data Structure & Algorithm', 'Average marks in Computer Organisation']=>['Average marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']: 100.0%

['Average marks in Data Structure & Algorithm', 'Average marks in Computer Organisation', 'Average marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Data Structure & Algorithm'", "'Object Oriented Programming'")

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture', 'Average marks in Object Oriented Programming']: 93.54838709677419%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture']=>['Average marks in Object Oriented Programming']: 96.66666666666667%

['Average marks in Data Structure & Algorithm', 'Average marks in Computer Organisation']=>['Average marks in Computer Architecture', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Average marks in Computer Organisation', 'Average marks in Computer Architecture']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Organisation'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'")

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 96.7741935483871%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Computer Organisation'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Object Oriented Programming'")

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 96.7741935483871%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Average marks in Computer Organisation']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Average marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Organisation'", "'Data Structure & Algorithm'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 96.7741935483871%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 96.7741935483871%

Relationship between: ("'Communication Engg & Coding Theory'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Object Oriented Programming'")

['Average marks in Data Structure & Algorithm', 'Good marks in Communication Engg & Coding Theory']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 95.65217391304348%

['Average marks in Data Structure & Algorithm', 'Good marks in Communication Engg & Coding Theory', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Average marks in Communication Engg & Coding Theory']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Average marks in Communication Engg & Coding Theory', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Compiler Design'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Formal Language & Automata Theory'")

['Average marks in Data Structure & Algorithm', 'Good marks in Formal Language & Automata Theory']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Compiler Design']: 100.0%

['Average marks in Data Structure & Algorithm', 'Good marks in Formal Language & Automata Theory', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Compiler Design']: 100.0%

['Average marks in Data Structure & Algorithm', 'Average marks in Formal Language & Automata Theory']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Compiler Design']: 89.47368421052632%

['Average marks in Data Structure & Algorithm', 'Average marks in Formal Language & Automata Theory', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Compiler Design']: 94.44444444444444%

Relationship between: ("'Compiler Design'", "'Data Structure & Algorithm'", "'Formal Language & Automata Theory'", "'Object Oriented Programming'")

['Average marks in Data Structure & Algorithm', 'Good marks in Formal Language & Automata Theory']=>['Average marks in Object Oriented Programming', 'Good marks in Compiler Design']: 100.0%

['Average marks in Data Structure & Algorithm', 'Good marks in Formal Language & Automata Theory', 'Average marks in Object Oriented Programming']=>['Good marks in Compiler Design']: 100.0%

['Average marks in Data Structure & Algorithm', 'Average marks in Formal Language & Automata Theory']=>['Average marks in Object Oriented Programming', 'Average marks in Compiler Design']: 89.47368421052632%

['Average marks in Data Structure & Algorithm', 'Average marks in Formal Language & Automata Theory', 'Average marks in Object Oriented Programming']=>['Average marks in Compiler Design']: 94.44444444444444%

Relationship between: ("'Computer Architecture'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'")

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 90.625%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 93.54838709677419%

Relationship between: ("'Computer Architecture'", "'Data Structure & Algorithm'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 90.625%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 96.66666666666667%

Relationship between: ("'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Discrete Mathematics'", "'Object Oriented Programming'")

['Average marks in Data Structure & Algorithm', 'Average marks in Design & Analysis of Algorithm', 'Good marks in Discrete Mathematics']=>['Average marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Average marks in Design & Analysis of Algorithm', 'Average marks in Discrete Mathematics']=>['Average marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Average marks in Design & Analysis of Algorithm', 'Poor marks in Discrete Mathematics']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'")

['Good marks in Computer Organisation', 'Good marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 100.0%

['Good marks in Computer Organisation', 'Good marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

['Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 100.0%

['Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

['Good marks in Computer Organisation', 'Poor marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 80.95238095238095%

['Good marks in Computer Organisation', 'Poor marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 94.44444444444444%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Design & Analysis of Algorithm'", "'Numerical Methods'")

['Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']: 100.0%

['Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Numerical Methods'", "'Object Oriented Programming'")

['Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Average marks in Object Oriented Programming']: 100.0%

['Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Organisation'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'")

['Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 100.0%

['Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Computer Organisation'", "'Design & Analysis of Algorithm'", "'Numerical Methods'", "'Object Oriented Programming'")

['Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Organisation'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'", "'Object Oriented Programming'")

['Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 100.0%

['Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Communication Engg & Coding Theory'", "'Computer Architecture'", "'Computer Organisation'", "'Microprocessors & Microcontrollers'")

['Good marks in Computer Organisation', 'Good marks in Communication Engg & Coding Theory']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 87.5%

['Good marks in Computer Organisation', 'Good marks in Communication Engg & Coding Theory', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 95.45454545454545%

['Good marks in Computer Organisation', 'Average marks in Communication Engg & Coding Theory']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 94.11764705882352%

['Good marks in Computer Organisation', 'Average marks in Communication Engg & Coding Theory', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Formal Language & Automata Theory'", "'Microprocessors & Microcontrollers'")

['Good marks in Computer Organisation', 'Good marks in Formal Language & Automata Theory']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 95.45454545454545%

['Good marks in Computer Organisation', 'Good marks in Formal Language & Automata Theory', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

['Good marks in Computer Organisation', 'Average marks in Formal Language & Automata Theory']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 95.45454545454545%

['Good marks in Computer Organisation', 'Average marks in Formal Language & Automata Theory', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Compiler Design'", "'Computer Architecture'", "'Computer Organisation'", "'Formal Language & Automata Theory'")

['Good marks in Computer Organisation', 'Good marks in Formal Language & Automata Theory']=>['Good marks in Computer Architecture', 'Good marks in Compiler Design']: 90.9090909090909%

['Good marks in Computer Organisation', 'Good marks in Formal Language & Automata Theory', 'Good marks in Computer Architecture']=>['Good marks in Compiler Design']: 95.23809523809523%

['Good marks in Computer Organisation', 'Average marks in Formal Language & Automata Theory']=>['Good marks in Computer Architecture', 'Average marks in Compiler Design']: 86.36363636363636%

['Good marks in Computer Organisation', 'Average marks in Formal Language & Automata Theory', 'Good marks in Computer Architecture']=>['Average marks in Compiler Design']: 90.47619047619048%

Relationship between: ("'Compiler Design'", "'Computer Organisation'", "'Formal Language & Automata Theory'", "'Microprocessors & Microcontrollers'")

['Good marks in Computer Organisation', 'Good marks in Formal Language & Automata Theory']=>['Good marks in Microprocessors & Microcontrollers', 'Good marks in Compiler Design']: 95.45454545454545%

['Good marks in Computer Organisation', 'Good marks in Formal Language & Automata Theory', 'Good marks in Microprocessors & Microcontrollers']=>['Good marks in Compiler Design']: 95.45454545454545%

['Good marks in Computer Organisation', 'Average marks in Formal Language & Automata Theory']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Compiler Design']: 90.9090909090909%

['Good marks in Computer Organisation', 'Average marks in Formal Language & Automata Theory', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Compiler Design']: 90.9090909090909%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Design & Analysis of Algorithm'", "'Object Oriented Programming'")

['Good marks in Computer Organisation', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

['Average marks in Computer Organisation', 'Average marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Organisation'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Good marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 100.0%

['Good marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Communication Engg & Coding Theory'", "'Design & Analysis of Algorithm'", "'Numerical Methods'", "'Object Oriented Programming'")

['Average marks in Numerical Methods', 'Average marks in Communication Engg & Coding Theory']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Numerical Methods', 'Average marks in Communication Engg & Coding Theory', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Design & Analysis of Algorithm'", "'Formal Language & Automata Theory'", "'Numerical Methods'", "'Object Oriented Programming'")

['Average marks in Numerical Methods', 'Good marks in Formal Language & Automata Theory']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 94.73684210526315%

['Average marks in Numerical Methods', 'Good marks in Formal Language & Automata Theory', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Compiler Design'", "'Design & Analysis of Algorithm'", "'Formal Language & Automata Theory'", "'Numerical Methods'")

['Average marks in Numerical Methods', 'Good marks in Formal Language & Automata Theory']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Compiler Design']: 94.73684210526315%

['Average marks in Numerical Methods', 'Good marks in Formal Language & Automata Theory', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Compiler Design']: 100.0%

Relationship between: ("'Compiler Design'", "'Formal Language & Automata Theory'", "'Numerical Methods'", "'Object Oriented Programming'")

['Average marks in Numerical Methods', 'Good marks in Formal Language & Automata Theory']=>['Average marks in Object Oriented Programming', 'Good marks in Compiler Design']: 94.73684210526315%

['Average marks in Numerical Methods', 'Good marks in Formal Language & Automata Theory', 'Average marks in Object Oriented Programming']=>['Good marks in Compiler Design']: 100.0%

Relationship between: ("'Computer Architecture'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'")

['Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 90.0%

['Average marks in Numerical Methods', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 90.0%

Relationship between: ("'Computer Architecture'", "'Design & Analysis of Algorithm'", "'Numerical Methods'", "'Object Oriented Programming'")

['Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Numerical Methods', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Architecture'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'", "'Object Oriented Programming'")

['Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 90.0%

['Average marks in Numerical Methods', 'Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Compiler Design'", "'Computer Architecture'", "'Formal Language & Automata Theory'", "'Microprocessors & Microcontrollers'")

['Good marks in Formal Language & Automata Theory', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers', 'Good marks in Compiler Design']: 87.5%

['Good marks in Formal Language & Automata Theory', 'Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']=>['Good marks in Compiler Design']: 95.45454545454545%

['Average marks in Formal Language & Automata Theory', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Compiler Design']: 86.36363636363636%

['Average marks in Formal Language & Automata Theory', 'Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Compiler Design']: 90.47619047619048%

Relationship between: ("'Compiler Design'", "'Design & Analysis of Algorithm'", "'Formal Language & Automata Theory'", "'Object Oriented Programming'")

['Good marks in Formal Language & Automata Theory', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming', 'Good marks in Compiler Design']: 100.0%

['Good marks in Formal Language & Automata Theory', 'Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']=>['Good marks in Compiler Design']: 100.0%

['Average marks in Formal Language & Automata Theory', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming', 'Average marks in Compiler Design']: 95.23809523809523%

['Average marks in Formal Language & Automata Theory', 'Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']=>['Average marks in Compiler Design']: 95.23809523809523%

Relationship between: ("'Computer Architecture'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Good marks in Computer Architecture', 'Good marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers', 'Good marks in Object Oriented Programming']: 93.75%

['Good marks in Computer Architecture', 'Good marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Good marks in Object Oriented Programming']: 100.0%

['Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 94.11764705882352%

['Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Computer Organisation'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'")

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Organisation', 'Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 88.88888888888889%

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 94.11764705882352%

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 94.11764705882352%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Organisation', 'Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 95.23809523809523%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

['Poor marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Organisation', 'Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 88.23529411764706%

['Poor marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 88.23529411764706%

['Poor marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'")

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Numerical Methods', 'Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 83.33333333333334%

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 100.0%

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Numerical Methods', 'Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 85.71428571428571%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 94.73684210526315%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 94.73684210526315%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'")

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Architecture', 'Good marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 83.33333333333334%

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Architecture']=>['Good marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 83.33333333333334%

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Architecture', 'Good marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 95.23809523809523%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 95.23809523809523%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 95.23809523809523%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Object Oriented Programming'")

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Architecture', 'Good marks in Design & Analysis of Algorithm', 'Good marks in Object Oriented Programming']: 83.33333333333334%

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Architecture']=>['Good marks in Design & Analysis of Algorithm', 'Good marks in Object Oriented Programming']: 83.33333333333334%

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Architecture', 'Good marks in Design & Analysis of Algorithm']=>['Good marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers', 'Good marks in Object Oriented Programming']: 83.33333333333334%

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers', 'Good marks in Object Oriented Programming']: 83.33333333333334%

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']=>['Good marks in Object Oriented Programming']: 88.23529411764706%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 95.23809523809523%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 95.23809523809523%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers', 'Good marks in Object Oriented Programming']: 83.33333333333334%

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers', 'Good marks in Object Oriented Programming']: 100.0%

['Good marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Good marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 95.23809523809523%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 95.23809523809523%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Numerical Methods'", "'Object Oriented Programming'")

['Good marks in Introduction to Computing']=>['Good marks in Data Structure & Algorithm', 'Good marks in Numerical Methods', 'Good marks in Design & Analysis of Algorithm', 'Good marks in Object Oriented Programming']: 76.47058823529412%

['Good marks in Introduction to Computing', 'Good marks in Data Structure & Algorithm']=>['Good marks in Numerical Methods', 'Good marks in Design & Analysis of Algorithm', 'Good marks in Object Oriented Programming']: 89.65517241379311%

['Good marks in Introduction to Computing', 'Good marks in Data Structure & Algorithm', 'Good marks in Numerical Methods']=>['Good marks in Design & Analysis of Algorithm', 'Good marks in Object Oriented Programming']: 100.0%

['Good marks in Introduction to Computing', 'Good marks in Data Structure & Algorithm', 'Good marks in Numerical Methods', 'Good marks in Design & Analysis of Algorithm']=>['Good marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing']=>['Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 85.0%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm']=>['Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 94.44444444444444%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

['Poor marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm']=>['Poor marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 94.73684210526315%

['Poor marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Poor marks in Numerical Methods']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Poor marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Poor marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

['Poor marks in Introduction to Computing', 'Poor marks in Data Structure & Algorithm']=>['Poor marks in Numerical Methods', 'Poor marks in Design & Analysis of Algorithm', 'Poor marks in Object Oriented Programming']: 88.23529411764706%

['Poor marks in Introduction to Computing', 'Poor marks in Data Structure & Algorithm', 'Poor marks in Numerical Methods']=>['Poor marks in Design & Analysis of Algorithm', 'Poor marks in Object Oriented Programming']: 100.0%

['Poor marks in Introduction to Computing', 'Poor marks in Data Structure & Algorithm', 'Poor marks in Numerical Methods', 'Poor marks in Design & Analysis of Algorithm']=>['Poor marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Architecture'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Object Oriented Programming'")

['Good marks in Introduction to Computing', 'Good marks in Data Structure & Algorithm', 'Good marks in Computer Architecture']=>['Good marks in Design & Analysis of Algorithm', 'Good marks in Object Oriented Programming']: 93.75%

['Good marks in Introduction to Computing', 'Good marks in Data Structure & Algorithm', 'Good marks in Computer Architecture', 'Good marks in Design & Analysis of Algorithm']=>['Good marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Good marks in Introduction to Computing', 'Good marks in Data Structure & Algorithm', 'Good marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Good marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Architecture'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Good marks in Introduction to Computing', 'Good marks in Computer Architecture']=>['Good marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers', 'Good marks in Object Oriented Programming']: 78.94736842105263%

['Good marks in Introduction to Computing', 'Good marks in Computer Architecture', 'Good marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers', 'Good marks in Object Oriented Programming']: 93.75%

['Good marks in Introduction to Computing', 'Good marks in Computer Architecture', 'Good marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Good marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 90.9090909090909%

['Average marks in Introduction to Computing', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 90.9090909090909%

['Average marks in Introduction to Computing', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Organisation'", "'Data Structure & Algorithm'", "'Introduction to Computing'", "'Numerical Methods'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods']: 76.19047619047619%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Good marks in Computer Organisation', 'Average marks in Numerical Methods']: 84.21052631578947%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Average marks in Numerical Methods']: 88.88888888888889%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Computer Organisation'", "'Data Structure & Algorithm'", "'Introduction to Computing'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture']: 85.71428571428571%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Good marks in Computer Organisation', 'Good marks in Computer Architecture']: 94.73684210526315%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Organisation'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm']: 85.71428571428571%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Good marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm']: 94.73684210526315%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Organisation'", "'Data Structure & Algorithm'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Good marks in Microprocessors & Microcontrollers']: 85.71428571428571%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Good marks in Computer Organisation', 'Good marks in Microprocessors & Microcontrollers']: 94.73684210526315%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Organisation'", "'Data Structure & Algorithm'", "'Introduction to Computing'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Average marks in Object Oriented Programming']: 85.71428571428571%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Good marks in Computer Organisation', 'Average marks in Object Oriented Programming']: 94.73684210526315%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Data Structure & Algorithm'", "'Introduction to Computing'", "'Numerical Methods'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']: 80.95238095238095%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Average marks in Numerical Methods', 'Good marks in Computer Architecture']: 89.47368421052632%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Numerical Methods'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']: 80.95238095238095%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']: 89.47368421052632%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Data Structure & Algorithm'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods', 'Good marks in Microprocessors & Microcontrollers']: 76.19047619047619%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Average marks in Numerical Methods', 'Good marks in Microprocessors & Microcontrollers']: 84.21052631578947%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods']=>['Good marks in Microprocessors & Microcontrollers']: 94.11764705882352%

Relationship between: ("'Analog & Digital Electronics'", "'Data Structure & Algorithm'", "'Introduction to Computing'", "'Numerical Methods'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods', 'Average marks in Object Oriented Programming']: 80.95238095238095%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Average marks in Numerical Methods', 'Average marks in Object Oriented Programming']: 89.47368421052632%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']: 90.47619047619048%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Data Structure & Algorithm'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 85.71428571428571%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 94.73684210526315%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 94.73684210526315%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Data Structure & Algorithm'", "'Introduction to Computing'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture', 'Average marks in Object Oriented Programming']: 90.47619047619048%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Good marks in Computer Architecture', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Data Structure & Algorithm', 'Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 85.71428571428571%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 94.73684210526315%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 94.73684210526315%

Relationship between: ("'Analog & Digital Electronics'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Data Structure & Algorithm', 'Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 90.47619047619048%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Data Structure & Algorithm'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Data Structure & Algorithm', 'Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 85.71428571428571%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 94.73684210526315%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Computer Organisation'", "'Introduction to Computing'", "'Numerical Methods'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']: 85.71428571428571%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation']=>['Average marks in Numerical Methods', 'Good marks in Computer Architecture']: 90.0%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Organisation'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Numerical Methods'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']: 85.71428571428571%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation']=>['Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']: 90.0%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Organisation'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Good marks in Microprocessors & Microcontrollers']: 85.71428571428571%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation']=>['Average marks in Numerical Methods', 'Good marks in Microprocessors & Microcontrollers']: 90.0%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

['Poor marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Organisation', 'Poor marks in Numerical Methods', 'Good marks in Microprocessors & Microcontrollers']: 94.11764705882352%

['Poor marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation']=>['Poor marks in Numerical Methods', 'Good marks in Microprocessors & Microcontrollers']: 94.11764705882352%

['Poor marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Poor marks in Numerical Methods']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Organisation'", "'Introduction to Computing'", "'Numerical Methods'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Average marks in Object Oriented Programming']: 85.71428571428571%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation']=>['Average marks in Numerical Methods', 'Average marks in Object Oriented Programming']: 90.0%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Computer Organisation'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Organisation', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']: 95.23809523809523%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Computer Organisation'", "'Introduction to Computing'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Organisation', 'Good marks in Computer Architecture', 'Average marks in Object Oriented Programming']: 95.23809523809523%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Organisation'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 95.23809523809523%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Organisation'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 95.23809523809523%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Organisation'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Good marks in Computer Organisation', 'Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 95.23809523809523%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Numerical Methods'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Numerical Methods', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']: 90.47619047619048%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Introduction to Computing'", "'Numerical Methods'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Numerical Methods', 'Good marks in Computer Architecture', 'Average marks in Object Oriented Programming']: 90.47619047619048%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 85.71428571428571%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 94.73684210526315%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 94.73684210526315%

Relationship between: ("'Analog & Digital Electronics'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Numerical Methods'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 90.47619047619048%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics']=>['Average marks in Numerical Methods', 'Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 85.71428571428571%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 94.73684210526315%

['Average marks in Introduction to Computing', 'Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Data Structure & Algorithm'", "'Introduction to Computing'", "'Numerical Methods'")

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Average marks in Numerical Methods', 'Good marks in Computer Architecture']: 88.88888888888889%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture']: 100.0%

Relationship between: ("'Computer Organisation'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Numerical Methods'")

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']: 88.88888888888889%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Computer Organisation'", "'Data Structure & Algorithm'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'")

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Average marks in Numerical Methods', 'Good marks in Microprocessors & Microcontrollers']: 88.88888888888889%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Computer Organisation'", "'Data Structure & Algorithm'", "'Introduction to Computing'", "'Numerical Methods'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Average marks in Numerical Methods', 'Average marks in Object Oriented Programming']: 88.88888888888889%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'")

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']: 100.0%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Data Structure & Algorithm'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'")

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 100.0%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Data Structure & Algorithm'", "'Introduction to Computing'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Organisation'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'")

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 100.0%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Computer Organisation'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Organisation'", "'Data Structure & Algorithm'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Communication Engg & Coding Theory'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Average marks in Communication Engg & Coding Theory']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Average marks in Communication Engg & Coding Theory', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Formal Language & Automata Theory'", "'Introduction to Computing'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Formal Language & Automata Theory']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Formal Language & Automata Theory', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Compiler Design'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Formal Language & Automata Theory'", "'Introduction to Computing'")

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Formal Language & Automata Theory']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Compiler Design']: 100.0%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Formal Language & Automata Theory', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Compiler Design']: 100.0%

Relationship between: ("'Compiler Design'", "'Data Structure & Algorithm'", "'Formal Language & Automata Theory'", "'Introduction to Computing'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Formal Language & Automata Theory']=>['Average marks in Object Oriented Programming', 'Good marks in Compiler Design']: 100.0%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Formal Language & Automata Theory', 'Average marks in Object Oriented Programming']=>['Good marks in Compiler Design']: 100.0%

Relationship between: ("'Computer Architecture'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'")

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 90.0%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 90.0%

Relationship between: ("'Computer Architecture'", "'Data Structure & Algorithm'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 90.0%

['Average marks in Introduction to Computing', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Numerical Methods'")

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation']=>['Average marks in Numerical Methods', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']: 90.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'")

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation']=>['Average marks in Numerical Methods', 'Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 90.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Introduction to Computing'", "'Numerical Methods'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation']=>['Average marks in Numerical Methods', 'Good marks in Computer Architecture', 'Average marks in Object Oriented Programming']: 90.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Organisation'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'")

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation']=>['Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 90.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Computer Organisation'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Numerical Methods'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation']=>['Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 90.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Organisation'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation']=>['Average marks in Numerical Methods', 'Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 90.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'")

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Organisation'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Communication Engg & Coding Theory'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Numerical Methods'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Average marks in Numerical Methods', 'Average marks in Communication Engg & Coding Theory']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Average marks in Numerical Methods', 'Average marks in Communication Engg & Coding Theory', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Design & Analysis of Algorithm'", "'Formal Language & Automata Theory'", "'Introduction to Computing'", "'Numerical Methods'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Average marks in Numerical Methods', 'Good marks in Formal Language & Automata Theory']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 94.11764705882352%

['Average marks in Introduction to Computing', 'Average marks in Numerical Methods', 'Good marks in Formal Language & Automata Theory', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Compiler Design'", "'Design & Analysis of Algorithm'", "'Formal Language & Automata Theory'", "'Introduction to Computing'", "'Numerical Methods'")

['Average marks in Introduction to Computing', 'Average marks in Numerical Methods', 'Good marks in Formal Language & Automata Theory']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Compiler Design']: 94.11764705882352%

['Average marks in Introduction to Computing', 'Average marks in Numerical Methods', 'Good marks in Formal Language & Automata Theory', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Compiler Design']: 100.0%

Relationship between: ("'Compiler Design'", "'Formal Language & Automata Theory'", "'Introduction to Computing'", "'Numerical Methods'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Average marks in Numerical Methods', 'Good marks in Formal Language & Automata Theory']=>['Average marks in Object Oriented Programming', 'Good marks in Compiler Design']: 94.11764705882352%

['Average marks in Introduction to Computing', 'Average marks in Numerical Methods', 'Good marks in Formal Language & Automata Theory', 'Average marks in Object Oriented Programming']=>['Good marks in Compiler Design']: 100.0%

Relationship between: ("'Computer Architecture'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'")

['Average marks in Introduction to Computing', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 90.0%

['Average marks in Introduction to Computing', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 90.0%

Relationship between: ("'Computer Architecture'", "'Design & Analysis of Algorithm'", "'Introduction to Computing'", "'Numerical Methods'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Introduction to Computing', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Architecture'", "'Introduction to Computing'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 90.0%

['Average marks in Introduction to Computing', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Compiler Design'", "'Design & Analysis of Algorithm'", "'Formal Language & Automata Theory'", "'Introduction to Computing'", "'Object Oriented Programming'")

['Average marks in Introduction to Computing', 'Good marks in Formal Language & Automata Theory']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming', 'Good marks in Compiler Design']: 94.73684210526315%

['Average marks in Introduction to Computing', 'Good marks in Formal Language & Automata Theory', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming', 'Good marks in Compiler Design']: 100.0%

['Average marks in Introduction to Computing', 'Good marks in Formal Language & Automata Theory', 'Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']=>['Good marks in Compiler Design']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Computer Organisation'", "'Data Structure & Algorithm'", "'Microprocessors & Microcontrollers'")

['Good marks in Analog & Digital Electronics', 'Good marks in Data Structure & Algorithm']=>['Good marks in Computer Organisation', 'Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 88.88888888888889%

['Good marks in Analog & Digital Electronics', 'Good marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 94.11764705882352%

['Good marks in Analog & Digital Electronics', 'Good marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 94.11764705882352%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Good marks in Computer Organisation', 'Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 93.75%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 96.7741935483871%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Computer Organisation'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'")

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Good marks in Computer Organisation', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']: 90.625%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']: 93.54838709677419%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm']: 96.66666666666667%

['Average marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Average marks in Computer Organisation', 'Average marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']: 100.0%

['Average marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Computer Organisation']=>['Average marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']: 100.0%

['Average marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Computer Organisation', 'Average marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Computer Organisation'", "'Data Structure & Algorithm'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Good marks in Computer Organisation', 'Good marks in Computer Architecture', 'Average marks in Object Oriented Programming']: 90.625%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture', 'Average marks in Object Oriented Programming']: 93.54838709677419%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture']=>['Average marks in Object Oriented Programming']: 96.66666666666667%

['Average marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Average marks in Computer Organisation', 'Average marks in Computer Architecture', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Computer Organisation']=>['Average marks in Computer Architecture', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Computer Organisation', 'Average marks in Computer Architecture']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Organisation'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'")

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Good marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 93.75%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 96.7741935483871%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Organisation'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Good marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 93.75%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 96.7741935483871%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

['Average marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Average marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Computer Organisation']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Organisation'", "'Data Structure & Algorithm'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Good marks in Computer Organisation', 'Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 93.75%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 96.7741935483871%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 96.7741935483871%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Numerical Methods'")

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']: 100.0%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Data Structure & Algorithm'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'")

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 94.11764705882352%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 94.11764705882352%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Data Structure & Algorithm'", "'Numerical Methods'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Average marks in Object Oriented Programming']: 100.0%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'")

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 94.11764705882352%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 94.11764705882352%

Relationship between: ("'Analog & Digital Electronics'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Numerical Methods'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Data Structure & Algorithm'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 94.11764705882352%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Communication Engg & Coding Theory'", "'Computer Architecture'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'")

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Communication Engg & Coding Theory']=>['Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']: 88.23529411764706%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Communication Engg & Coding Theory', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm']: 93.75%

Relationship between: ("'Analog & Digital Electronics'", "'Communication Engg & Coding Theory'", "'Computer Architecture'", "'Data Structure & Algorithm'", "'Microprocessors & Microcontrollers'")

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Communication Engg & Coding Theory']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 88.23529411764706%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Communication Engg & Coding Theory', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 93.75%

Relationship between: ("'Analog & Digital Electronics'", "'Communication Engg & Coding Theory'", "'Computer Architecture'", "'Data Structure & Algorithm'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Communication Engg & Coding Theory']=>['Good marks in Computer Architecture', 'Average marks in Object Oriented Programming']: 88.23529411764706%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Communication Engg & Coding Theory', 'Good marks in Computer Architecture']=>['Average marks in Object Oriented Programming']: 93.75%

Relationship between: ("'Analog & Digital Electronics'", "'Communication Engg & Coding Theory'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'")

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Communication Engg & Coding Theory']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 88.23529411764706%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Communication Engg & Coding Theory', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 93.75%

Relationship between: ("'Analog & Digital Electronics'", "'Communication Engg & Coding Theory'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Communication Engg & Coding Theory']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 94.11764705882352%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Communication Engg & Coding Theory', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Communication Engg & Coding Theory'", "'Data Structure & Algorithm'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Communication Engg & Coding Theory']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 88.23529411764706%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Communication Engg & Coding Theory', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 93.75%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'")

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 90.625%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 93.54838709677419%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 96.66666666666667%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 93.75%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 96.7741935483871%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

['Average marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Average marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Data Structure & Algorithm'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 90.625%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 93.54838709677419%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 96.66666666666667%

Relationship between: ("'Analog & Digital Electronics'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 93.75%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 96.7741935483871%

['Good marks in Analog & Digital Electronics', 'Average marks in Data Structure & Algorithm', 'Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Computer Organisation'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'")

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Good marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 100.0%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Good marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 100.0%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Poor marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 85.0%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Poor marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 94.44444444444444%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Computer Organisation'", "'Design & Analysis of Algorithm'", "'Numerical Methods'")

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']: 100.0%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Computer Organisation'", "'Numerical Methods'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Average marks in Object Oriented Programming']: 100.0%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Organisation'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'")

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 100.0%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Organisation'", "'Design & Analysis of Algorithm'", "'Numerical Methods'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Organisation'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 100.0%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Communication Engg & Coding Theory'", "'Computer Architecture'", "'Computer Organisation'", "'Microprocessors & Microcontrollers'")

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Good marks in Communication Engg & Coding Theory']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 91.30434782608695%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Good marks in Communication Engg & Coding Theory', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 95.45454545454545%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Communication Engg & Coding Theory']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 94.11764705882352%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Communication Engg & Coding Theory', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Computer Organisation'", "'Formal Language & Automata Theory'", "'Microprocessors & Microcontrollers'")

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Good marks in Formal Language & Automata Theory']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 95.45454545454545%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Good marks in Formal Language & Automata Theory', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Formal Language & Automata Theory']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 95.45454545454545%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Formal Language & Automata Theory', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Compiler Design'", "'Computer Architecture'", "'Computer Organisation'", "'Formal Language & Automata Theory'")

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Good marks in Formal Language & Automata Theory']=>['Good marks in Computer Architecture', 'Good marks in Compiler Design']: 90.9090909090909%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Good marks in Formal Language & Automata Theory', 'Good marks in Computer Architecture']=>['Good marks in Compiler Design']: 95.23809523809523%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Formal Language & Automata Theory']=>['Good marks in Computer Architecture', 'Average marks in Compiler Design']: 86.36363636363636%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Formal Language & Automata Theory', 'Good marks in Computer Architecture']=>['Average marks in Compiler Design']: 90.47619047619048%

Relationship between: ("'Analog & Digital Electronics'", "'Compiler Design'", "'Computer Organisation'", "'Formal Language & Automata Theory'", "'Microprocessors & Microcontrollers'")

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Good marks in Formal Language & Automata Theory']=>['Good marks in Microprocessors & Microcontrollers', 'Good marks in Compiler Design']: 95.45454545454545%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Good marks in Formal Language & Automata Theory', 'Good marks in Microprocessors & Microcontrollers']=>['Good marks in Compiler Design']: 95.45454545454545%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Formal Language & Automata Theory']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Compiler Design']: 90.9090909090909%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Formal Language & Automata Theory', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Compiler Design']: 90.9090909090909%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Computer Organisation'", "'Design & Analysis of Algorithm'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

['Average marks in Analog & Digital Electronics', 'Average marks in Computer Organisation', 'Average marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Organisation'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 100.0%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'")

['Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 94.73684210526315%

['Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 94.73684210526315%

['Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 94.73684210526315%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Design & Analysis of Algorithm'", "'Numerical Methods'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 94.73684210526315%

['Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 94.73684210526315%

['Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 94.73684210526315%

['Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 94.73684210526315%

['Good marks in Analog & Digital Electronics', 'Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Communication Engg & Coding Theory'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Good marks in Communication Engg & Coding Theory', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 93.75%

['Good marks in Analog & Digital Electronics', 'Good marks in Communication Engg & Coding Theory', 'Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Analog & Digital Electronics'", "'Compiler Design'", "'Computer Architecture'", "'Formal Language & Automata Theory'", "'Microprocessors & Microcontrollers'")

['Good marks in Analog & Digital Electronics', 'Good marks in Formal Language & Automata Theory']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers', 'Good marks in Compiler Design']: 91.30434782608695%

['Good marks in Analog & Digital Electronics', 'Good marks in Formal Language & Automata Theory', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers', 'Good marks in Compiler Design']: 95.45454545454545%

['Good marks in Analog & Digital Electronics', 'Good marks in Formal Language & Automata Theory', 'Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']=>['Good marks in Compiler Design']: 95.45454545454545%

['Good marks in Analog & Digital Electronics', 'Average marks in Formal Language & Automata Theory']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers', 'Average marks in Compiler Design']: 82.6086956521739%

['Good marks in Analog & Digital Electronics', 'Average marks in Formal Language & Automata Theory', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Compiler Design']: 86.36363636363636%

['Good marks in Analog & Digital Electronics', 'Average marks in Formal Language & Automata Theory', 'Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Compiler Design']: 90.47619047619048%

Relationship between: ("'Analog & Digital Electronics'", "'Computer Architecture'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Architecture', 'Good marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers', 'Good marks in Object Oriented Programming']: 100.0%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Architecture', 'Good marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Good marks in Object Oriented Programming']: 100.0%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 96.96969696969697%

['Good marks in Analog & Digital Electronics', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Numerical Methods'")

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']: 100.0%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm']: 100.0%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Data Structure & Algorithm'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'")

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 100.0%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Data Structure & Algorithm'", "'Numerical Methods'", "'Object Oriented Programming'")

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Organisation'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'")

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 100.0%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Computer Organisation'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Numerical Methods'", "'Object Oriented Programming'")

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Organisation'", "'Data Structure & Algorithm'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'", "'Object Oriented Programming'")

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Communication Engg & Coding Theory'", "'Computer Architecture'", "'Computer Organisation'", "'Data Structure & Algorithm'", "'Microprocessors & Microcontrollers'")

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Good marks in Communication Engg & Coding Theory']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']: 93.75%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Good marks in Communication Engg & Coding Theory', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Communication Engg & Coding Theory'", "'Computer Organisation'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'")

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Good marks in Communication Engg & Coding Theory']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 93.75%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Good marks in Communication Engg & Coding Theory', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Communication Engg & Coding Theory'", "'Computer Organisation'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Object Oriented Programming'")

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Good marks in Communication Engg & Coding Theory']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 93.75%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Good marks in Communication Engg & Coding Theory', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Communication Engg & Coding Theory'", "'Computer Organisation'", "'Data Structure & Algorithm'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Good marks in Communication Engg & Coding Theory']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 93.75%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Good marks in Communication Engg & Coding Theory', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 93.75%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'")

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 93.54838709677419%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 96.66666666666667%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Object Oriented Programming'")

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 93.54838709677419%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 96.66666666666667%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Average marks in Computer Organisation']=>['Average marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Average marks in Computer Organisation', 'Average marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Average marks in Computer Organisation', 'Average marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Data Structure & Algorithm'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 93.54838709677419%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 96.66666666666667%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 96.66666666666667%

Relationship between: ("'Computer Organisation'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 96.7741935483871%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Organisation', 'Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Communication Engg & Coding Theory'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Numerical Methods'", "'Object Oriented Programming'")

['Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods', 'Average marks in Communication Engg & Coding Theory']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods', 'Average marks in Communication Engg & Coding Theory', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Formal Language & Automata Theory'", "'Numerical Methods'", "'Object Oriented Programming'")

['Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods', 'Good marks in Formal Language & Automata Theory']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods', 'Good marks in Formal Language & Automata Theory', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Compiler Design'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Formal Language & Automata Theory'", "'Numerical Methods'")

['Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods', 'Good marks in Formal Language & Automata Theory']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Compiler Design']: 100.0%

['Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods', 'Good marks in Formal Language & Automata Theory', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Compiler Design']: 100.0%

Relationship between: ("'Compiler Design'", "'Data Structure & Algorithm'", "'Formal Language & Automata Theory'", "'Numerical Methods'", "'Object Oriented Programming'")

['Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods', 'Good marks in Formal Language & Automata Theory']=>['Average marks in Object Oriented Programming', 'Good marks in Compiler Design']: 100.0%

['Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods', 'Good marks in Formal Language & Automata Theory', 'Average marks in Object Oriented Programming']=>['Good marks in Compiler Design']: 100.0%

Relationship between: ("'Computer Architecture'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'")

['Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 88.88888888888889%

['Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 88.88888888888889%

Relationship between: ("'Computer Architecture'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Numerical Methods'", "'Object Oriented Programming'")

['Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Architecture'", "'Data Structure & Algorithm'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'", "'Object Oriented Programming'")

['Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 88.88888888888889%

['Average marks in Data Structure & Algorithm', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Communication Engg & Coding Theory'", "'Computer Architecture'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Object Oriented Programming'")

['Average marks in Data Structure & Algorithm', 'Good marks in Communication Engg & Coding Theory', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 93.75%

['Average marks in Data Structure & Algorithm', 'Good marks in Communication Engg & Coding Theory', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Compiler Design'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Formal Language & Automata Theory'", "'Object Oriented Programming'")

['Average marks in Data Structure & Algorithm', 'Good marks in Formal Language & Automata Theory']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming', 'Good marks in Compiler Design']: 100.0%

['Average marks in Data Structure & Algorithm', 'Good marks in Formal Language & Automata Theory', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming', 'Good marks in Compiler Design']: 100.0%

['Average marks in Data Structure & Algorithm', 'Good marks in Formal Language & Automata Theory', 'Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']=>['Good marks in Compiler Design']: 100.0%

['Average marks in Data Structure & Algorithm', 'Average marks in Formal Language & Automata Theory']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming', 'Average marks in Compiler Design']: 89.47368421052632%

['Average marks in Data Structure & Algorithm', 'Average marks in Formal Language & Automata Theory', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming', 'Average marks in Compiler Design']: 94.44444444444444%

['Average marks in Data Structure & Algorithm', 'Average marks in Formal Language & Automata Theory', 'Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']=>['Average marks in Compiler Design']: 94.44444444444444%

Relationship between: ("'Computer Architecture'", "'Data Structure & Algorithm'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 90.625%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 93.54838709677419%

['Average marks in Data Structure & Algorithm', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'")

['Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 100.0%

['Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']: 100.0%

['Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers']: 100.0%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Design & Analysis of Algorithm'", "'Numerical Methods'", "'Object Oriented Programming'")

['Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']: 100.0%

['Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'", "'Object Oriented Programming'")

['Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 100.0%

['Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 100.0%

['Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Computer Organisation'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'", "'Object Oriented Programming'")

['Good marks in Computer Organisation', 'Average marks in Numerical Methods']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 100.0%

['Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 100.0%

['Good marks in Computer Organisation', 'Average marks in Numerical Methods', 'Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Communication Engg & Coding Theory'", "'Computer Organisation'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Good marks in Computer Organisation', 'Good marks in Communication Engg & Coding Theory', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 100.0%

['Good marks in Computer Organisation', 'Good marks in Communication Engg & Coding Theory', 'Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Compiler Design'", "'Computer Architecture'", "'Computer Organisation'", "'Formal Language & Automata Theory'", "'Microprocessors & Microcontrollers'")

['Good marks in Computer Organisation', 'Good marks in Formal Language & Automata Theory']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers', 'Good marks in Compiler Design']: 90.9090909090909%

['Good marks in Computer Organisation', 'Good marks in Formal Language & Automata Theory', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers', 'Good marks in Compiler Design']: 95.23809523809523%

['Good marks in Computer Organisation', 'Good marks in Formal Language & Automata Theory', 'Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']=>['Good marks in Compiler Design']: 95.23809523809523%

['Good marks in Computer Organisation', 'Average marks in Formal Language & Automata Theory']=>['Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers', 'Average marks in Compiler Design']: 86.36363636363636%

['Good marks in Computer Organisation', 'Average marks in Formal Language & Automata Theory', 'Good marks in Computer Architecture']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Compiler Design']: 90.47619047619048%

['Good marks in Computer Organisation', 'Average marks in Formal Language & Automata Theory', 'Good marks in Computer Architecture', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Compiler Design']: 90.47619047619048%

Relationship between: ("'Computer Architecture'", "'Computer Organisation'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'", "'Object Oriented Programming'")

['Good marks in Computer Organisation', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 100.0%

['Good marks in Computer Organisation', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

Relationship between: ("'Compiler Design'", "'Design & Analysis of Algorithm'", "'Formal Language & Automata Theory'", "'Numerical Methods'", "'Object Oriented Programming'")

['Average marks in Numerical Methods', 'Good marks in Formal Language & Automata Theory']=>['Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming', 'Good marks in Compiler Design']: 94.73684210526315%

['Average marks in Numerical Methods', 'Good marks in Formal Language & Automata Theory', 'Average marks in Design & Analysis of Algorithm']=>['Average marks in Object Oriented Programming', 'Good marks in Compiler Design']: 100.0%

['Average marks in Numerical Methods', 'Good marks in Formal Language & Automata Theory', 'Average marks in Design & Analysis of Algorithm', 'Average marks in Object Oriented Programming']=>['Good marks in Compiler Design']: 100.0%

Relationship between: ("'Computer Architecture'", "'Design & Analysis of Algorithm'", "'Microprocessors & Microcontrollers'", "'Numerical Methods'", "'Object Oriented Programming'")

['Average marks in Numerical Methods', 'Good marks in Computer Architecture']=>['Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 90.0%

['Average marks in Numerical Methods', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm']=>['Good marks in Microprocessors & Microcontrollers', 'Average marks in Object Oriented Programming']: 90.0%

['Average marks in Numerical Methods', 'Good marks in Computer Architecture', 'Average marks in Design & Analysis of Algorithm', 'Good marks in Microprocessors & Microcontrollers']=>['Average marks in Object Oriented Programming']: 100.0%

[Finished in 14.442s]